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VIRTUAL EVENT, APRIL 11-12, 2023

Exploring Performance Paradigm of HMB NVMe SSD's

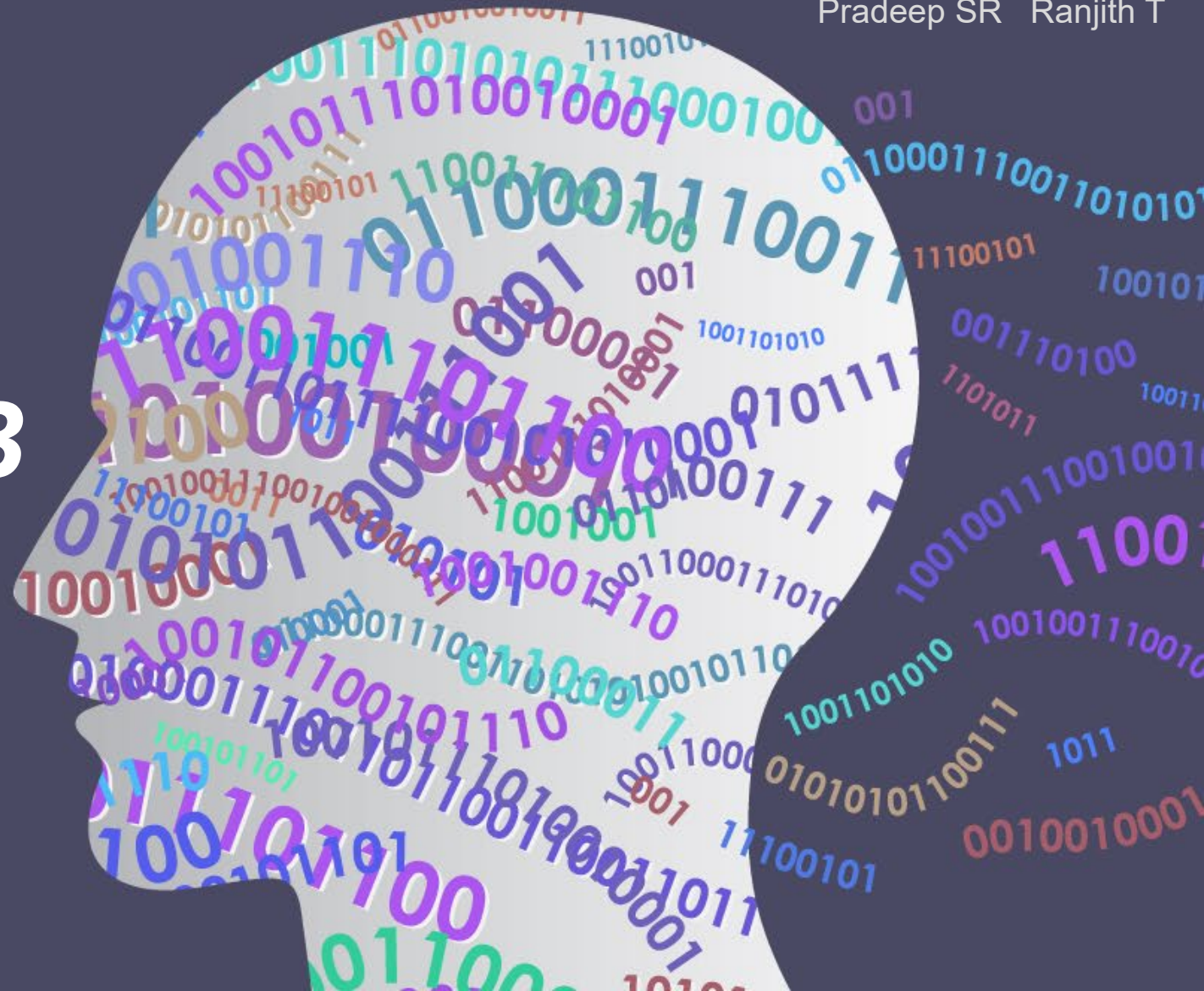
Presented by: Pradeep SR, Ranjith T
Samsung Electronics



Pradeep SR



Ranjith T



Disclaimer



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Agenda

- HMB Overview
- Performance Parameters Overview
- HMB vs DRAM Comparison
- HMB Tuning Comparison
- Conclusion



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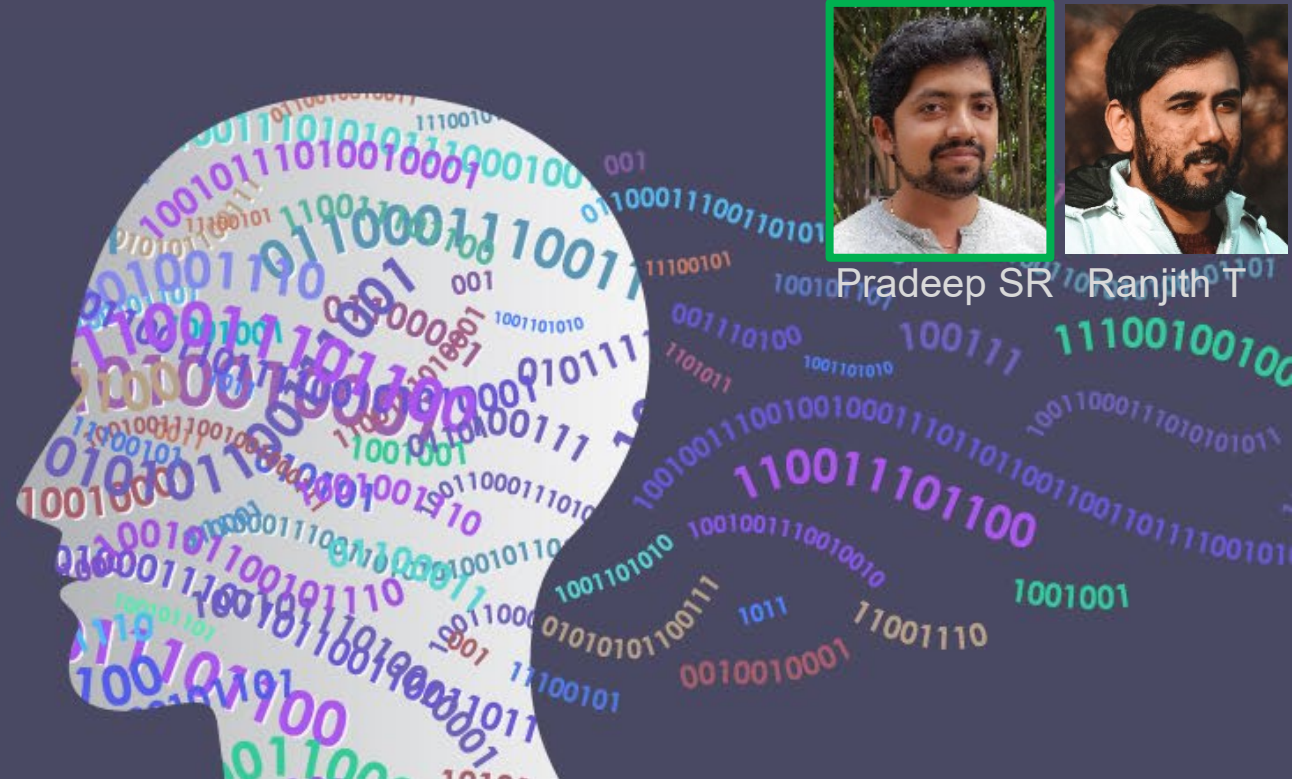


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HMB Overview

What & Why of HMB?

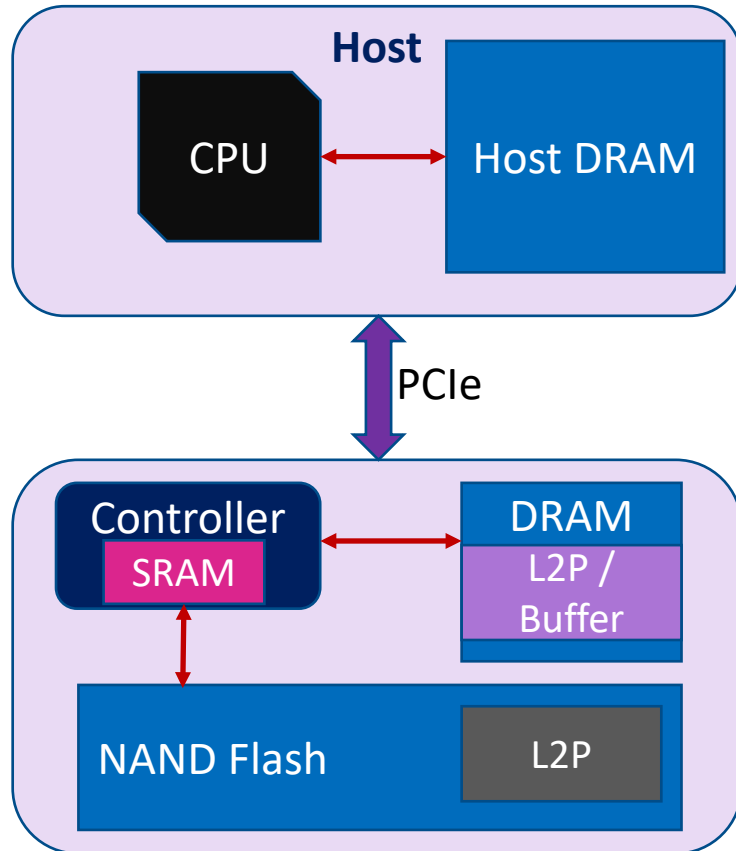
■ Host Memory Buffer (HMB)



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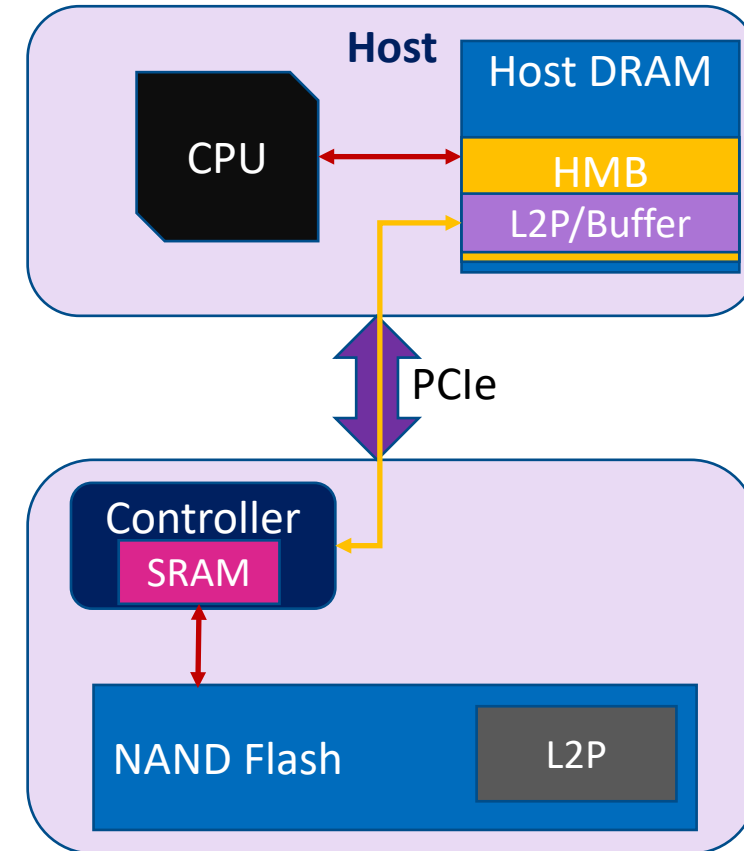
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Conventional SSD w/ DRAM

HMB Motivation:

- Cost Reduction
- Reduced Size
- Market Adoption



Value SSD w/ HMB

NVMe HMB Feature

- Device Requirements

- HMB Preferred Size (HMPRE)
- HMB Minimum Size (HMMIN)

- Host configuration of HMB

- Enable HMB
- HMB Size
- Descriptor Structure with each entry having Buffer address & chunk size
- Memory Return

- OS & Device Support

- Windows 10 OS & Above
- *Linux 5.12, Ubuntu 20.12 & Above
- NVMe 1.2 & above compliance devices

*Reference : <https://www.phoronix.com/review/samsung-980-linux>



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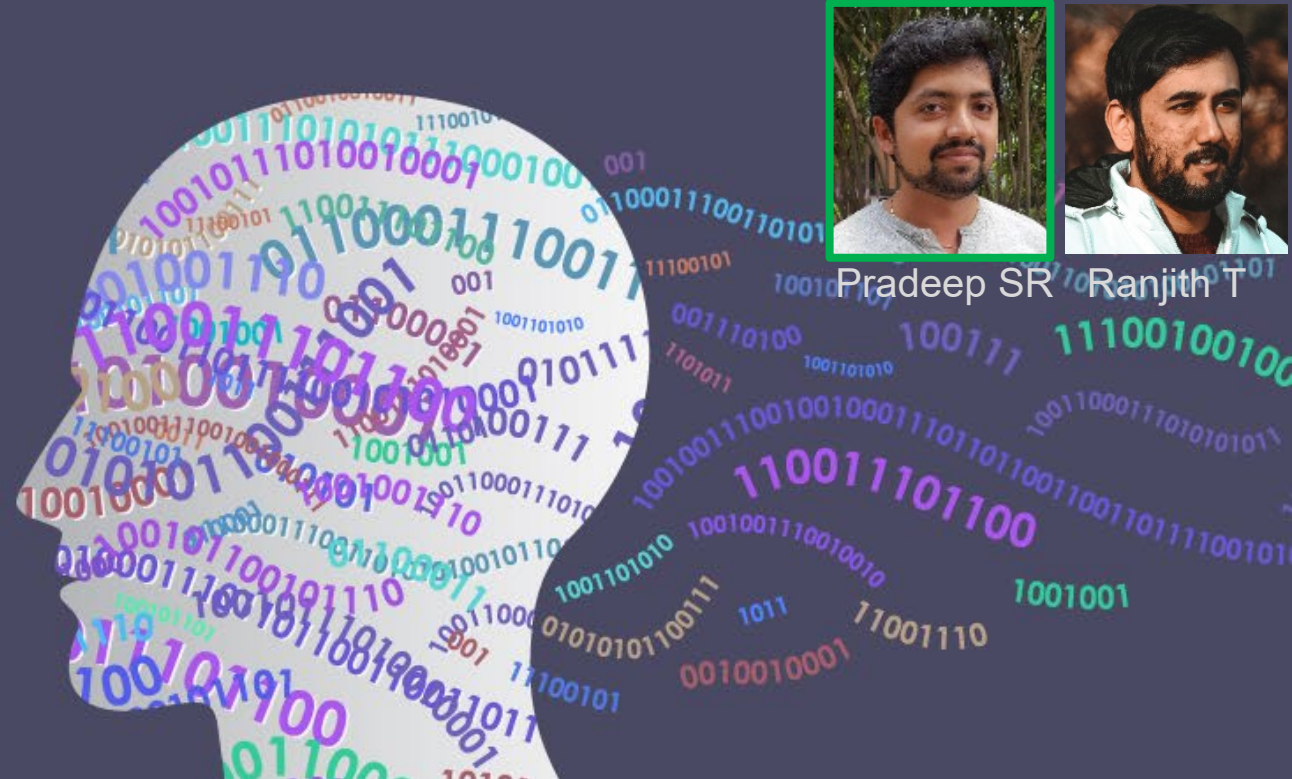


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Performance Parameters Overview

Key Factors

- Number of Queues
- Queue Depth
- IO Chunk size
- Workload types
- DRAM/ HMB Size
- Number of Threads



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Experiment set up



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Sl No	Item	Configuration
1	NVMe SSD “A” with DRAM	DRAM Size: 1GB
2	NVMe SSD “B” with HMB	HMBPRE Size: 64MB HMMIN Size: 16MB Note: Both “A” & “B” are of same Density & NAND Type, Controllers are different.
3	Device Driver	Custom Test Driver* Note: Custom driver used in order to override the Queue creation at initialization
4	Focus Area(s) of Experiment	▪ Number of Queue ▪ HMB Size allocation ▪ Drive States
5	Tool	IO Meter
6	Workloads	Standard Sequential & Random workloads
7	Host OS & DRAM Size	Windows 10 OS, 32GB DRAM

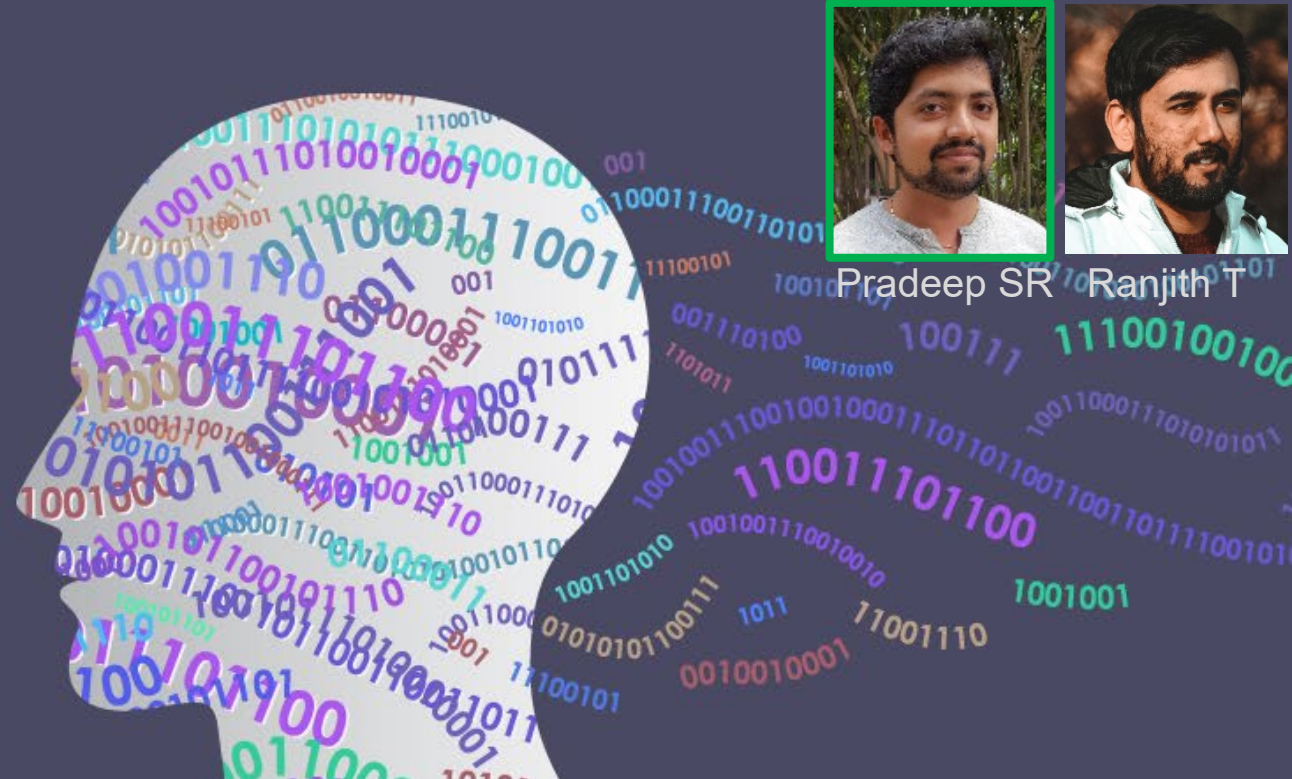
*Disclaimer(s):

- Custom Test Driver is in-house & not built for performance, thus numbers can vary across inbox driver.
- This Experiment purpose is to find behavior patterns Only



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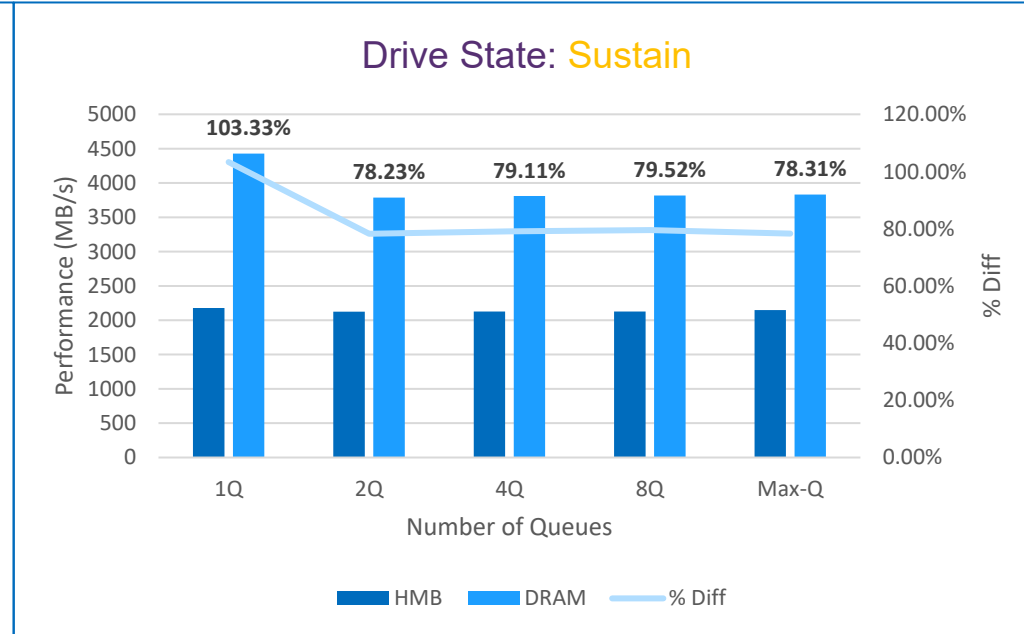
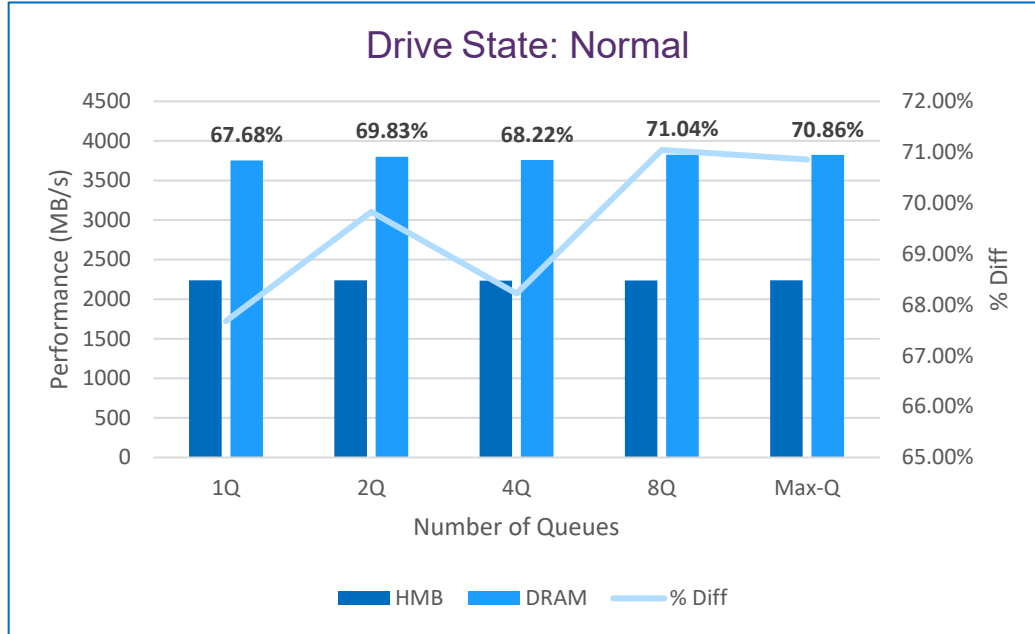


HMB vs DRAM Comparison

Workloads: Sequential Write (128K-T1-QD32)



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Key Observations:

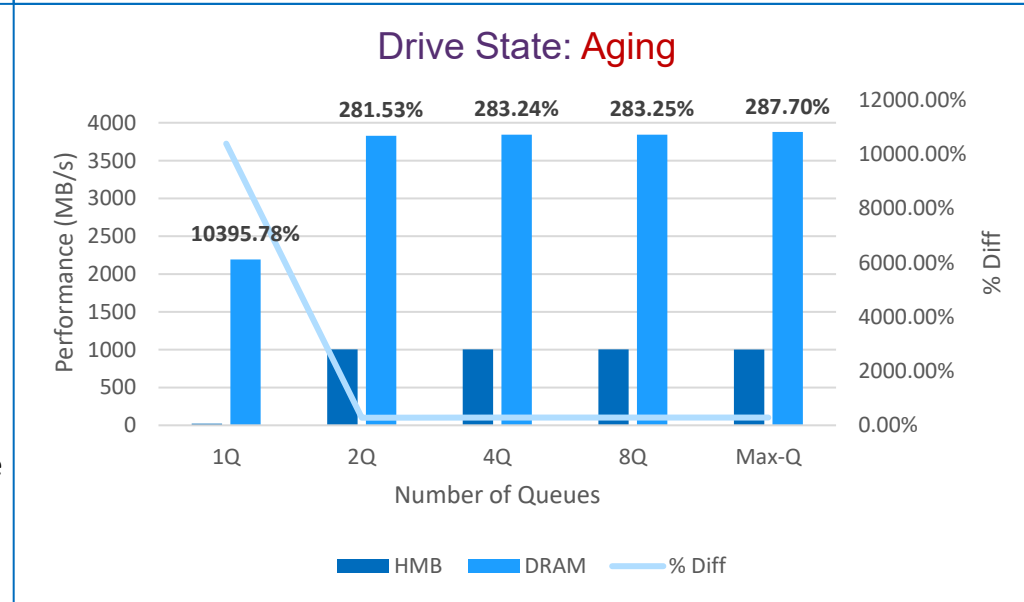
- DRAM device leads in all Drive states & gap widens multifold at aging
- HMB device at 1Q configuration shows lowest performance in Aging

Normal: No Pre-Condition

Sustain: Turbo area Pre-Conditioned

Aging: Full Drive Pre-conditioned

Max-Q: Max Queues Supported by Device

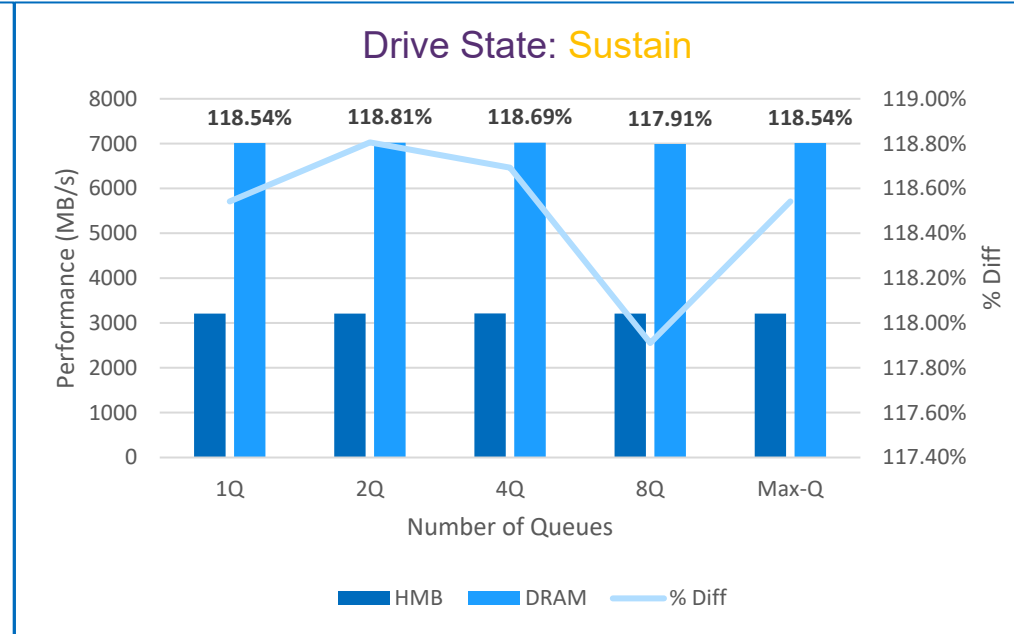
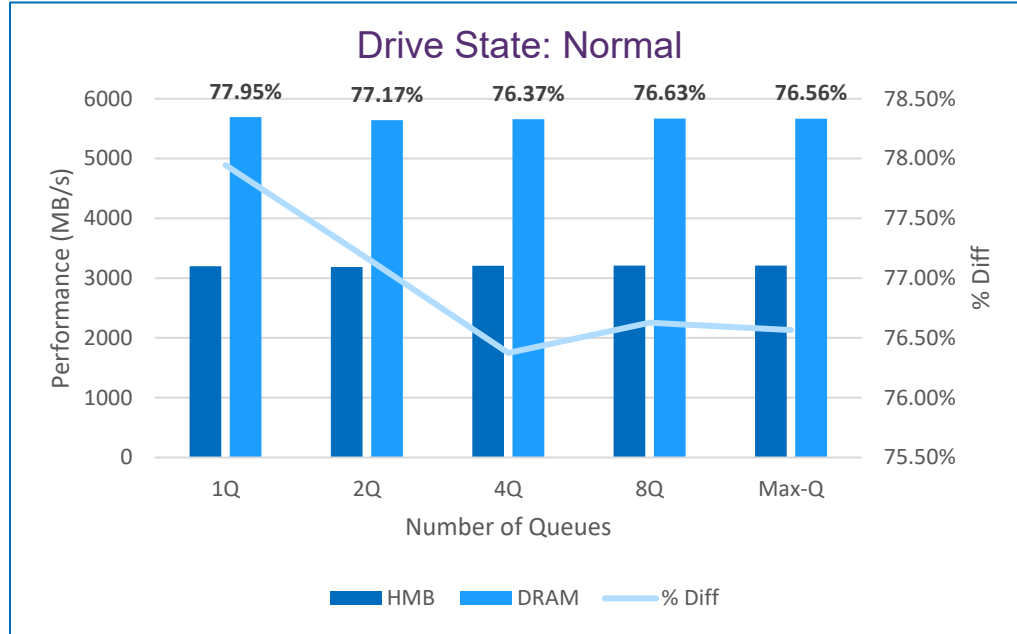


Disclaimer: Results are based on Custom test driver only

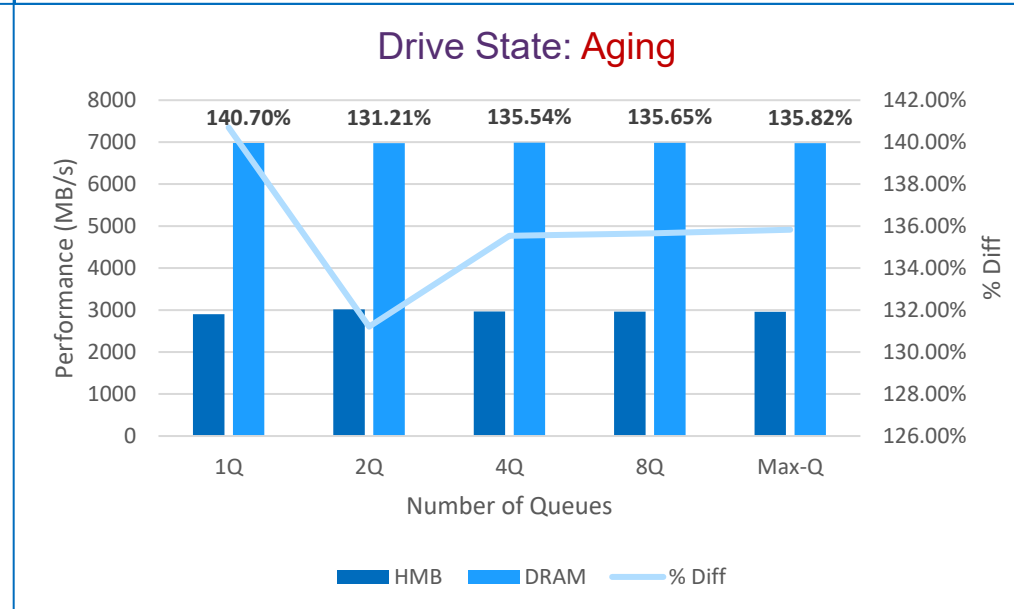
Workloads: Sequential Read (128K-T1-QD32)



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- **Key Observations:**
 - DRAM device leads in all Drive states & gap widens multifold at aging
 - 1Q configuration has better results for HMB device than in sequential write case in aging

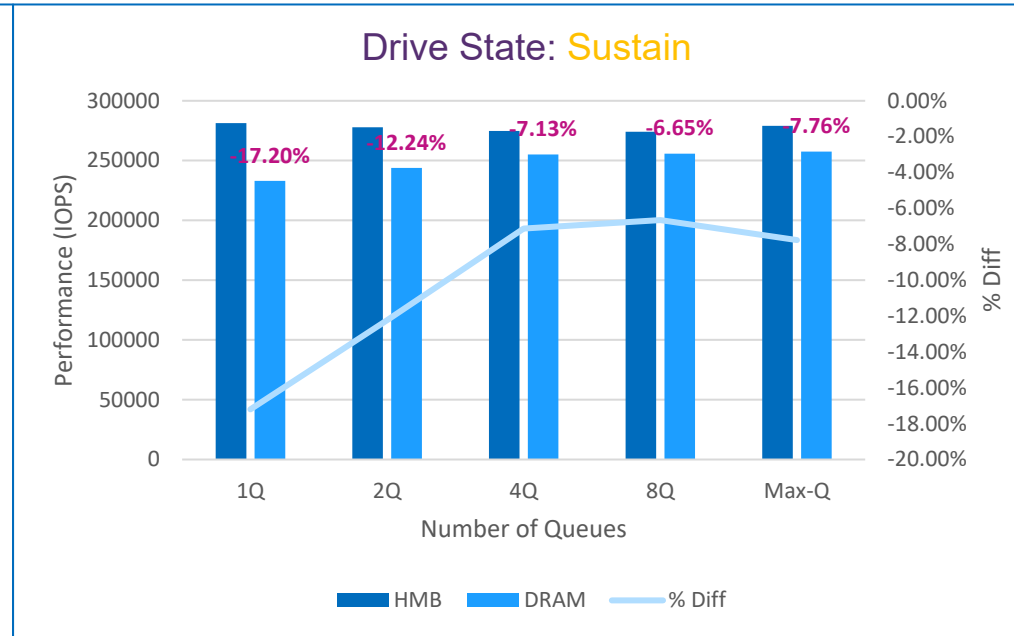
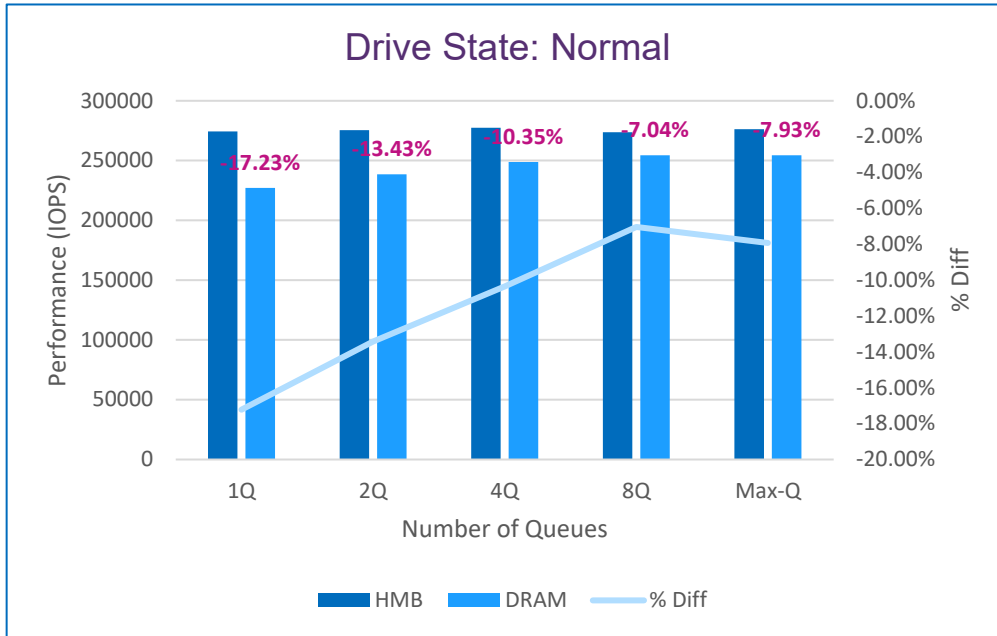


Disclaimer: Results are based on Custom test driver only

Workloads: Random Read (4K-T1-QD32)

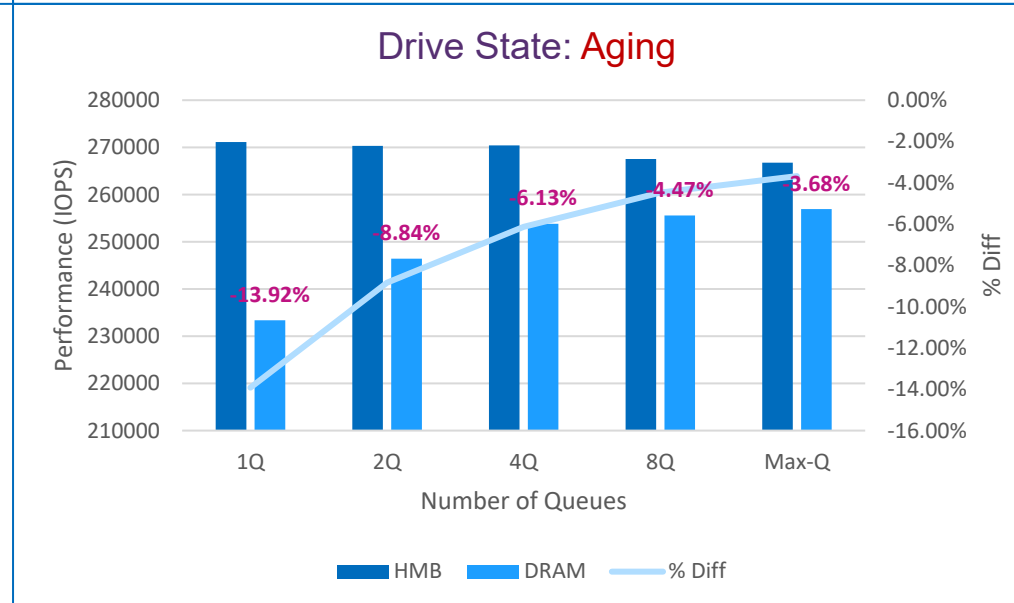


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Key Observations:

- HMB device performed better than DRAM device in all Drive states & Queue Configurations
- HMB is more Consistent at Aging state as well.
- In Aging with increase in number of Q's, DRAM is able to perform better

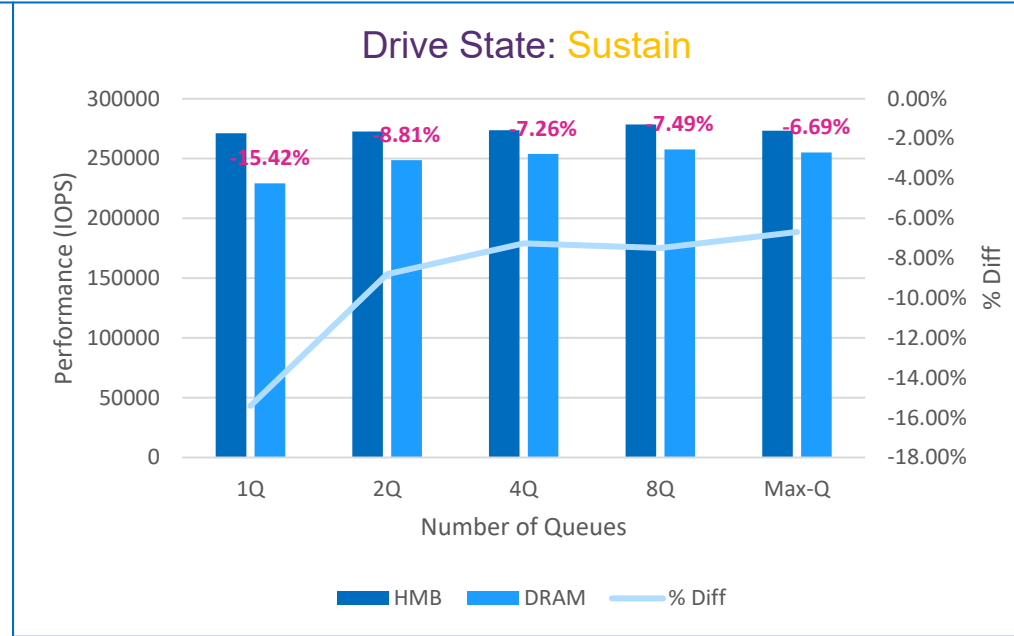
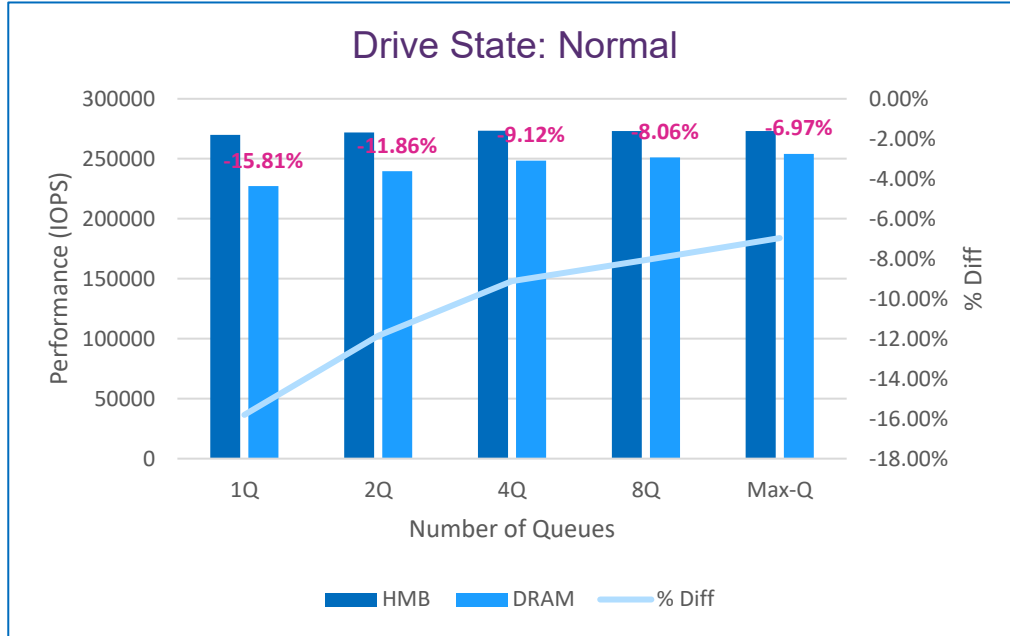


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Workloads: Random Read (4K-T16-QD32)

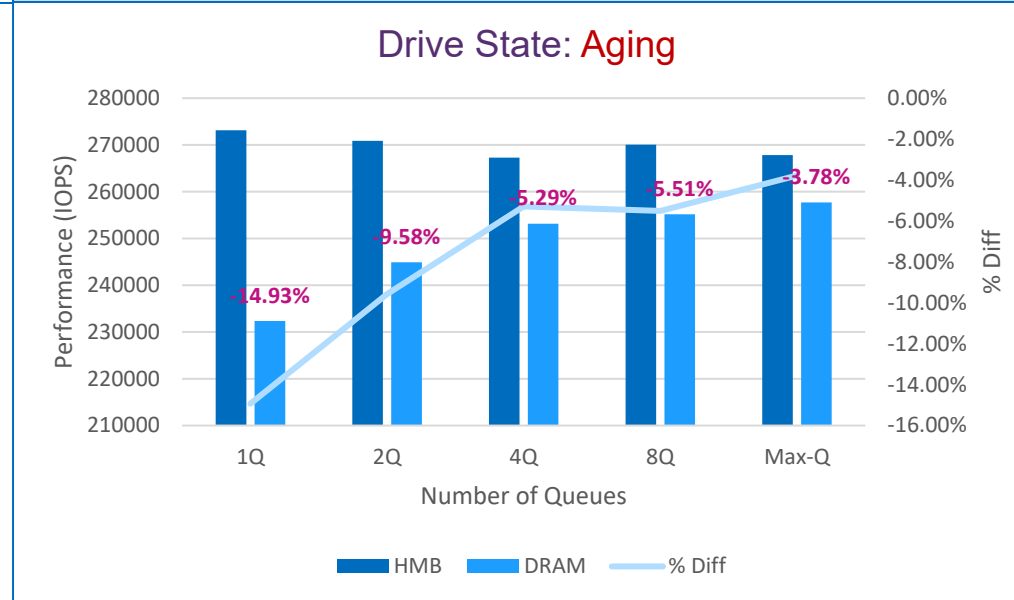


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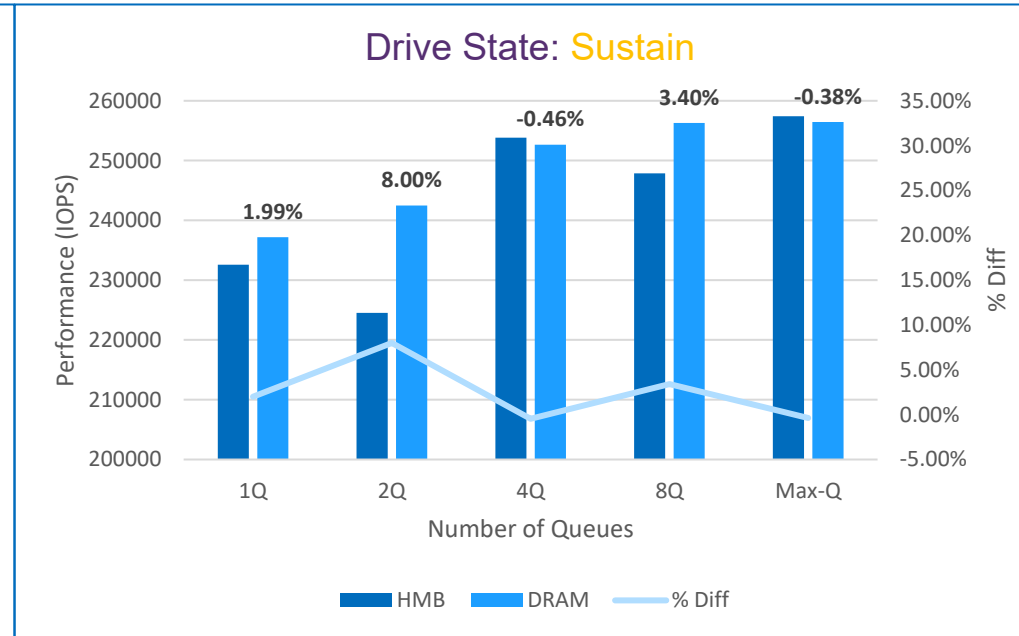
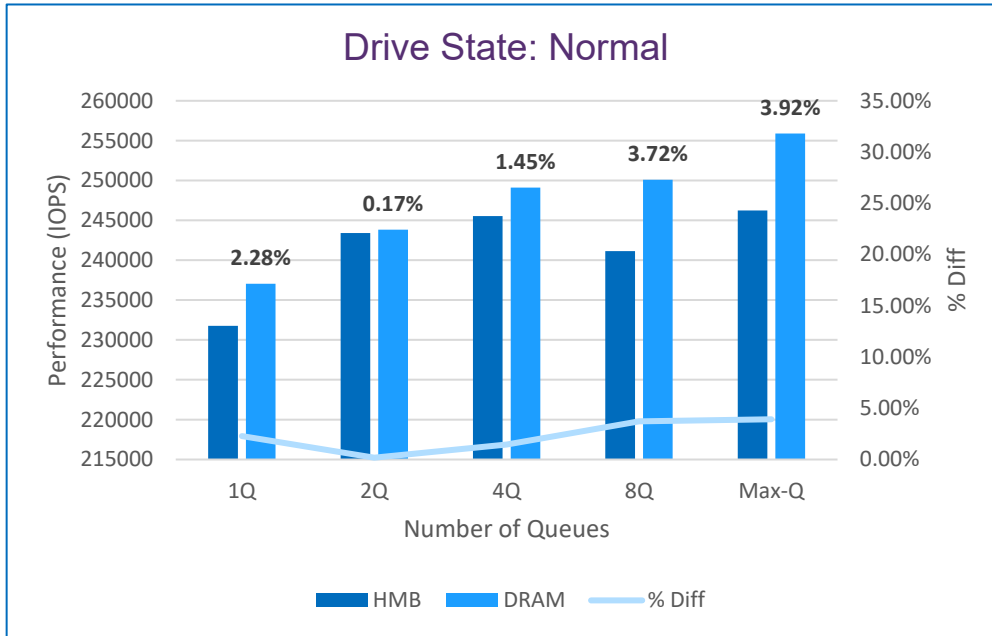


Disclaimer: Results are based on Custom test driver only

Workloads: Random Write (4K-T1-QD32)

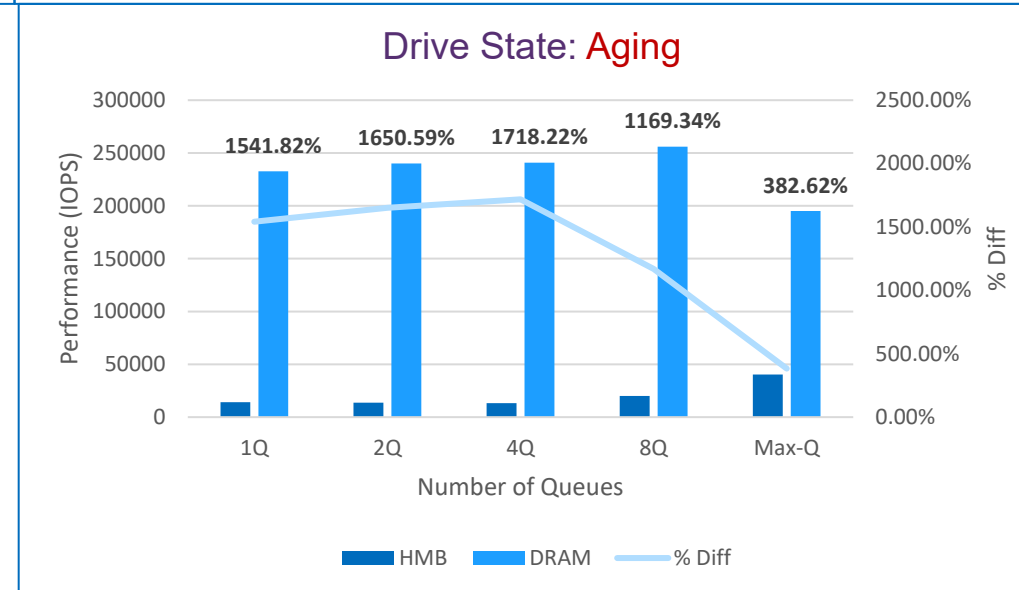


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Key Observations:

- In Normal & sustain States both device perform on par across majority of Queue states
- In Normal & Sustain state Performance increased as number of Q's increased
- In Aging DRAM device leads by large margin

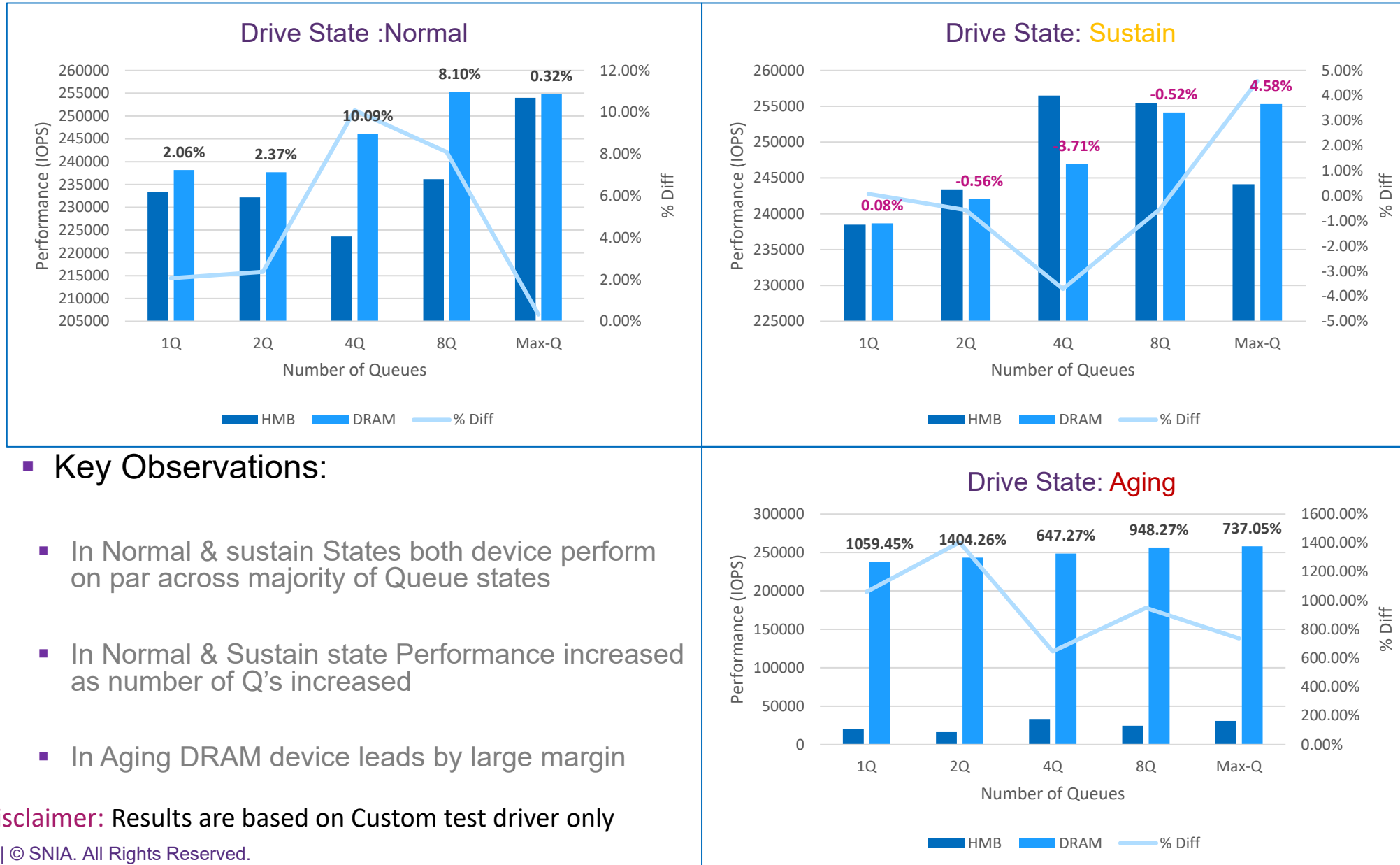


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Workloads: Random Write (4K-T16-QD32)



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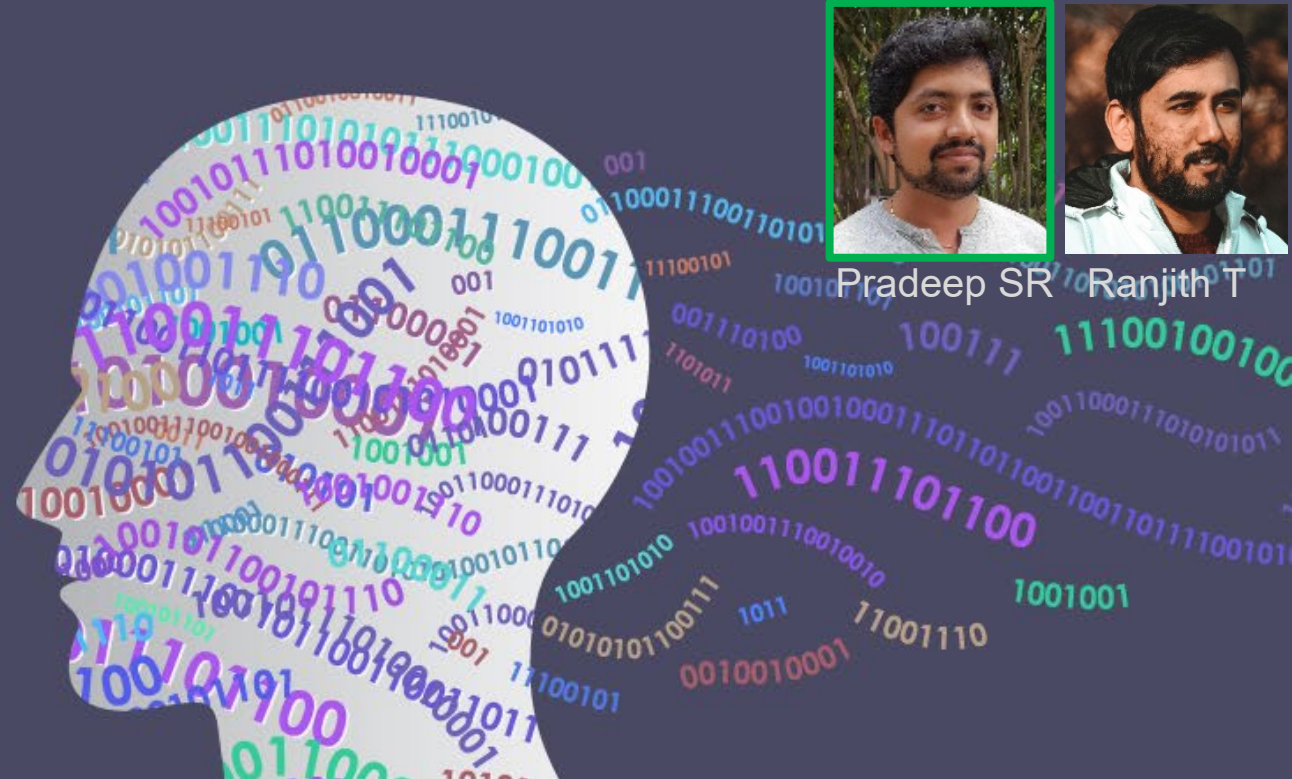


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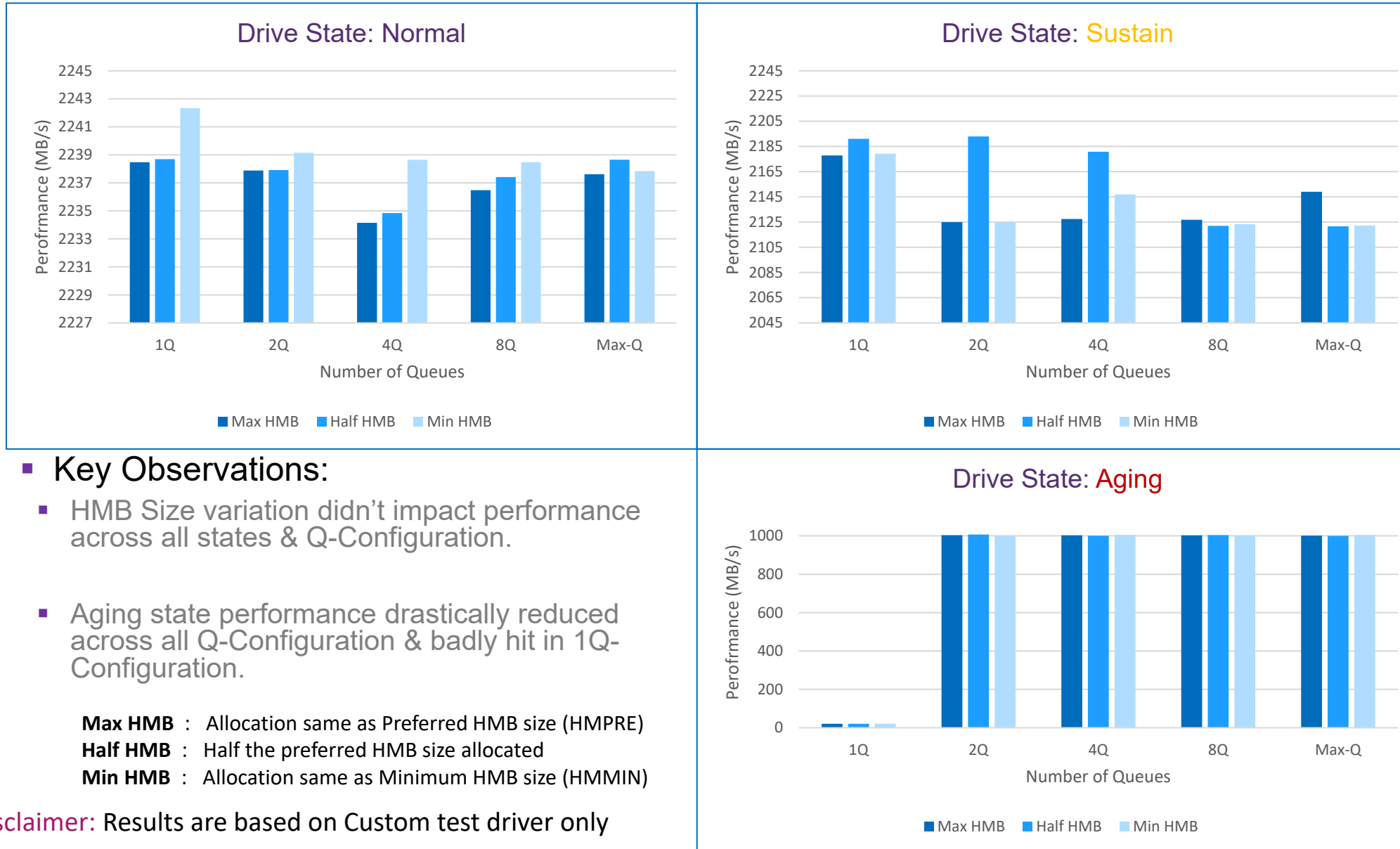


HMB Tuning Comparison

Workloads: Sequential Write (128K-T1-QD32)



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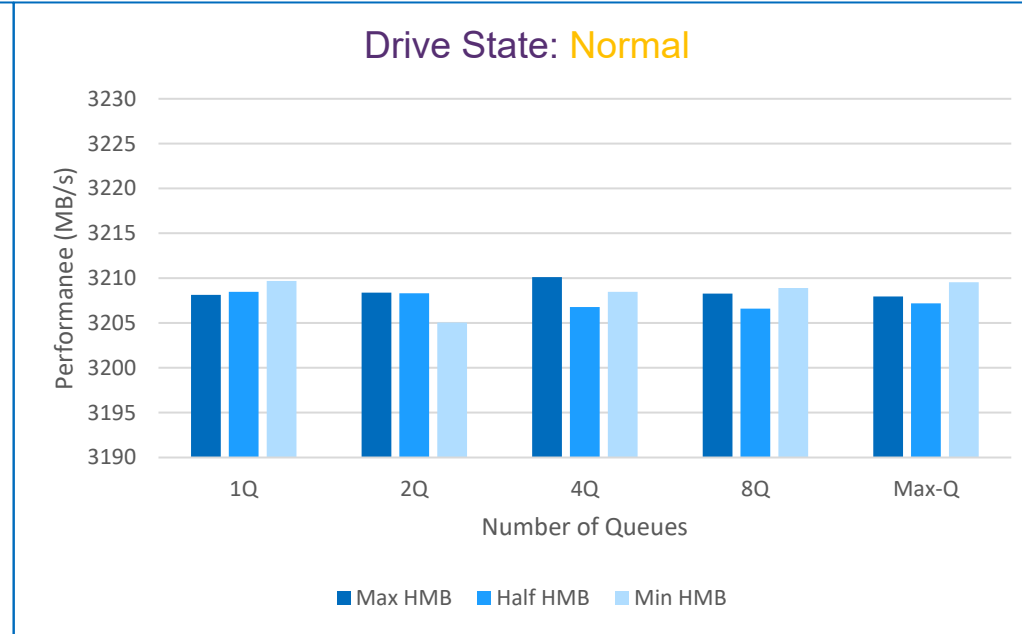
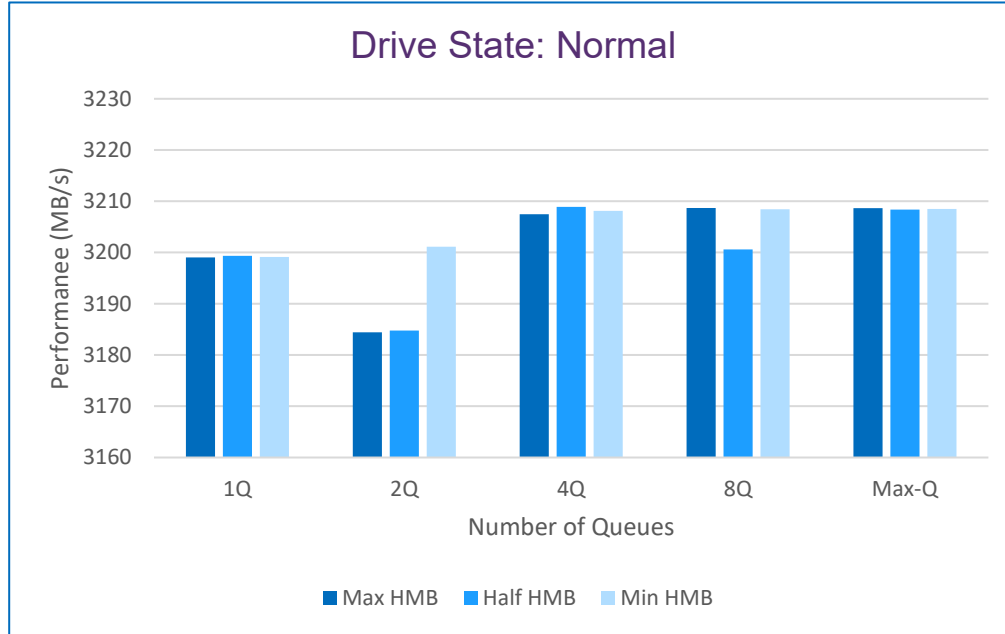


Disclaimer: Results are based on Custom test driver only

Workloads: Sequential Read (128K-T1-QD32)

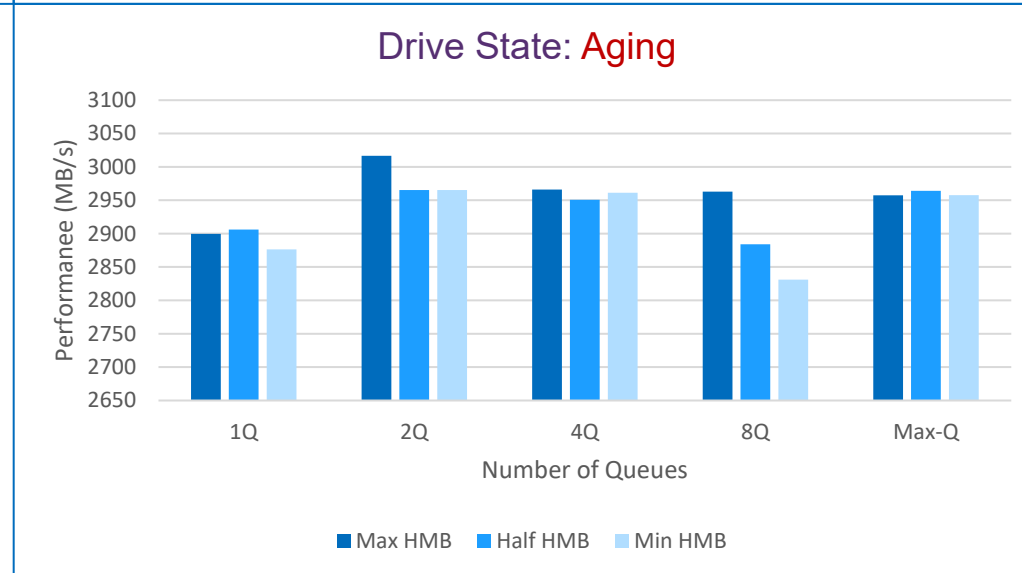


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■ Key Observations:

- HMB allocation changes didn't impact in Normal & Sustain states.
- In Aging 8Q configuration shows decrease in performance is proportional to decrease in HMB Allocation

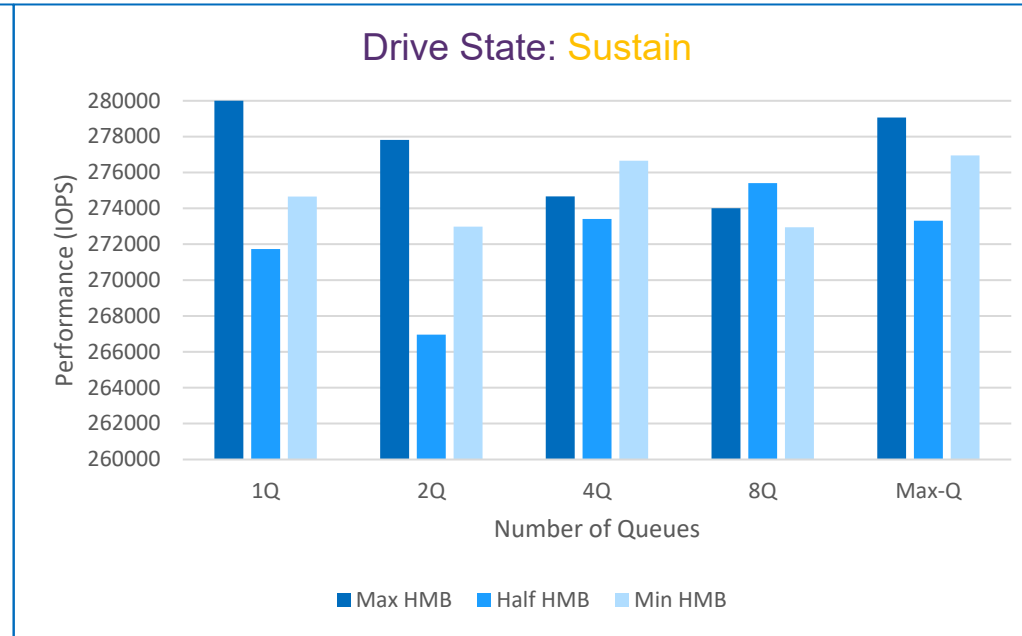
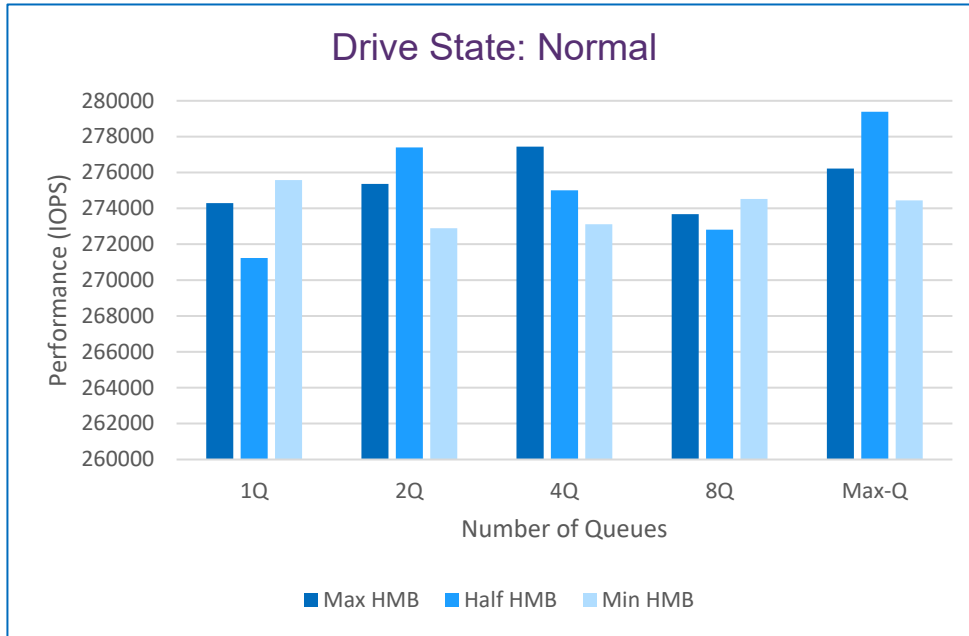


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Workloads: Random Read (4K-T1-QD32)

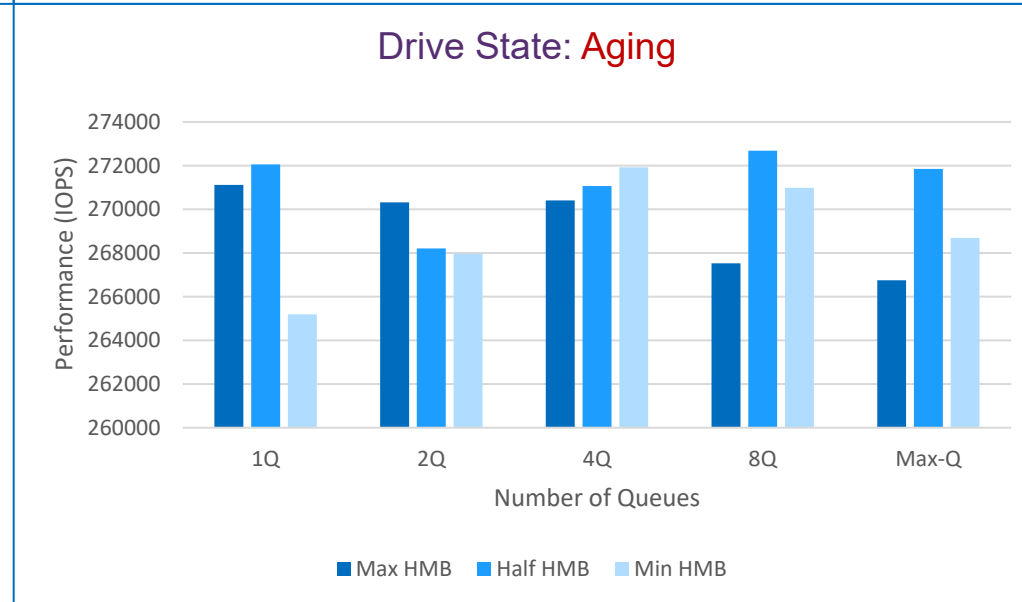


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Key Observations:

- Performance better for Max HMB allocation in majority cases.
- In Aging Half HMB size allocation is more consistent across all Queue configurations

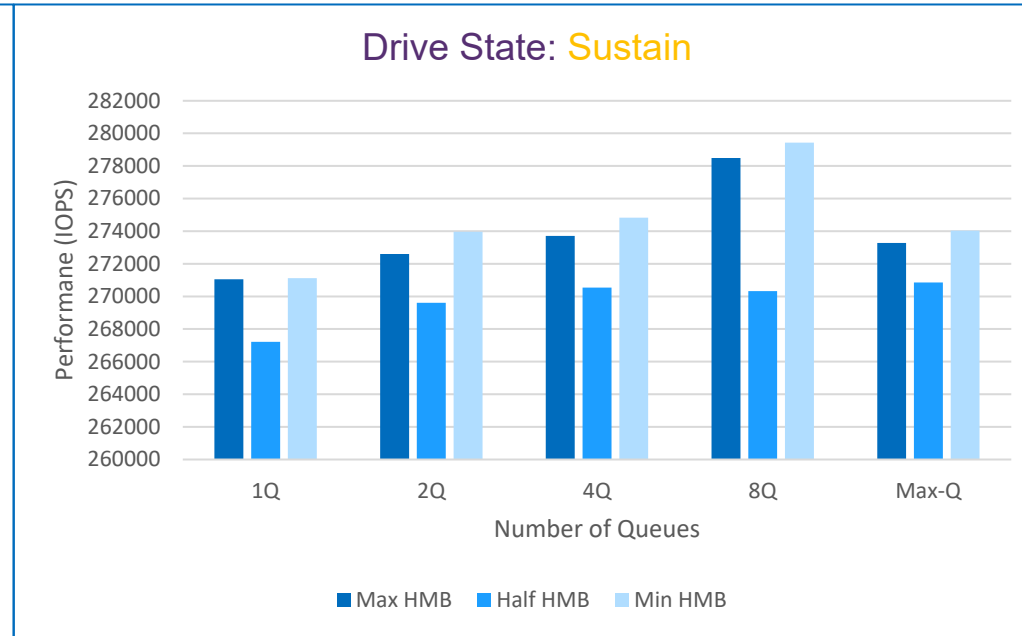
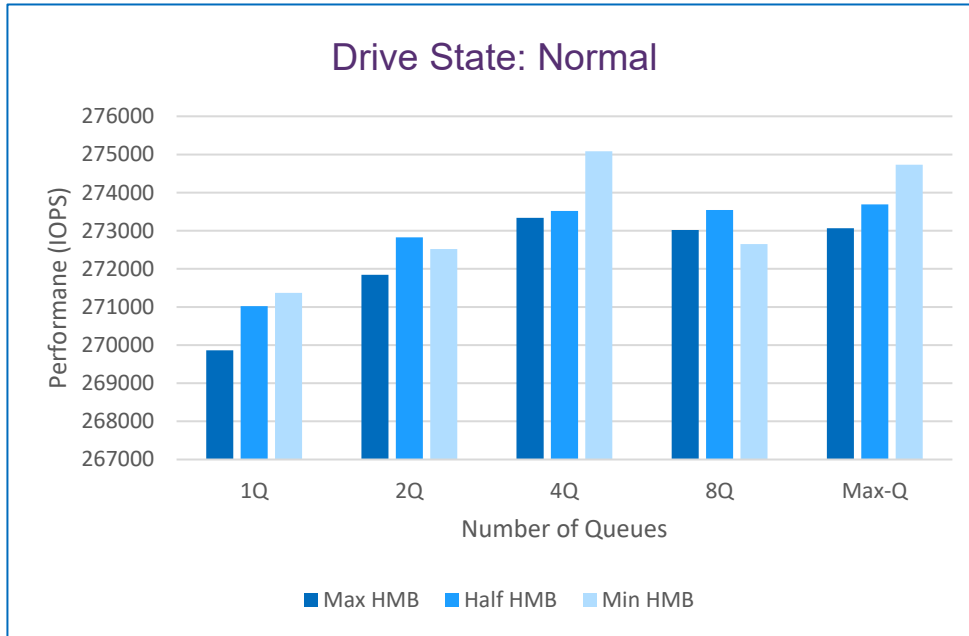


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Workloads: Random Read (4K-T16-QD32)

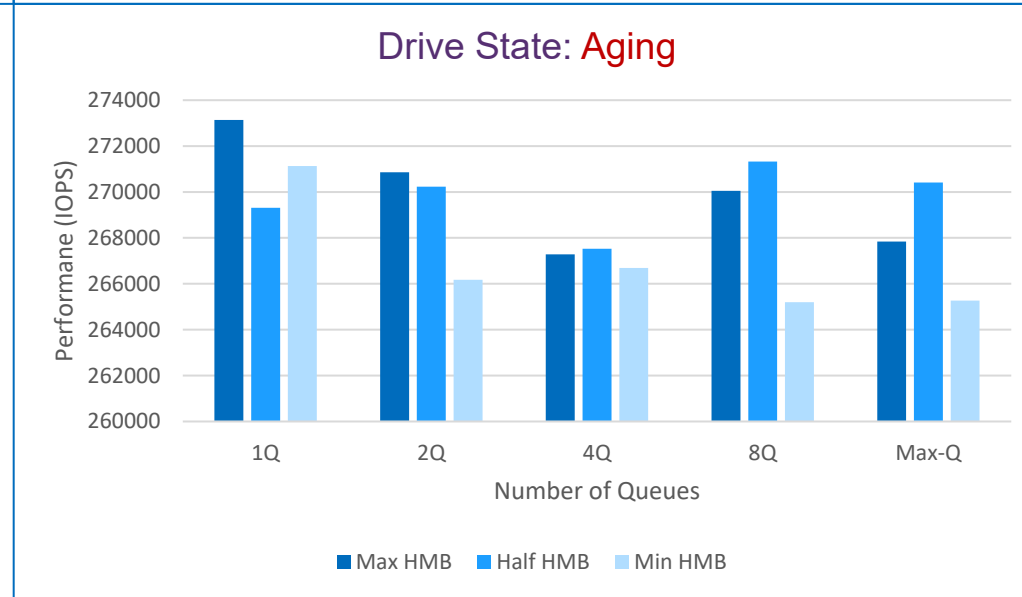


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Key Observations:

- HMB allocation Half the preferred size performed better in Normal & Aging states.
- Half HMB Size allocation worked relatively poor in sustain state across all Queue configurations
- Min HMB allocation shows Aging performance decreased with increase in Queues configured.

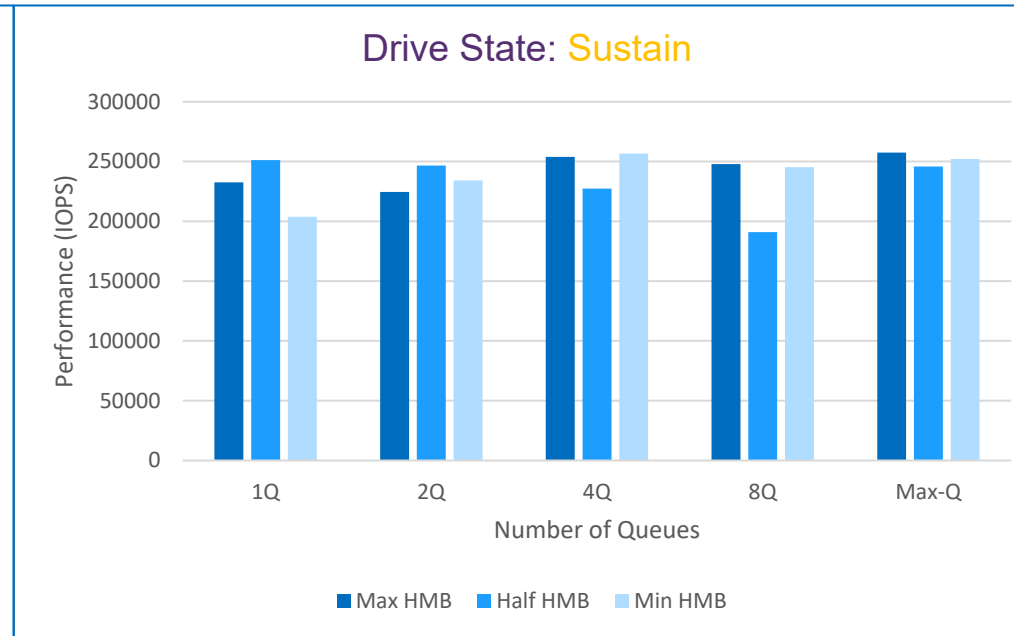
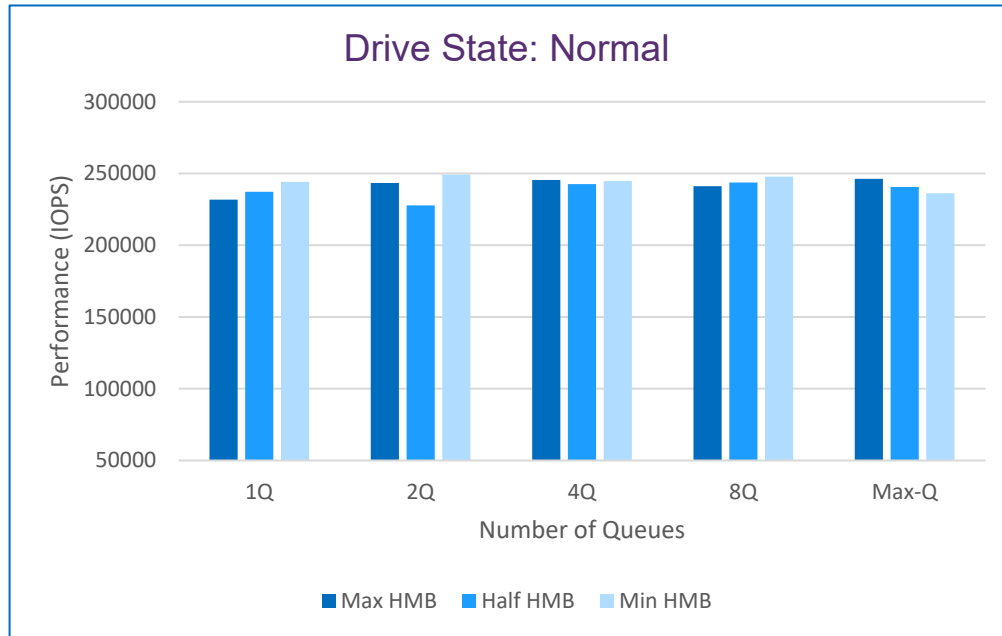


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Workloads: Random Write (4K-T1-QD32)

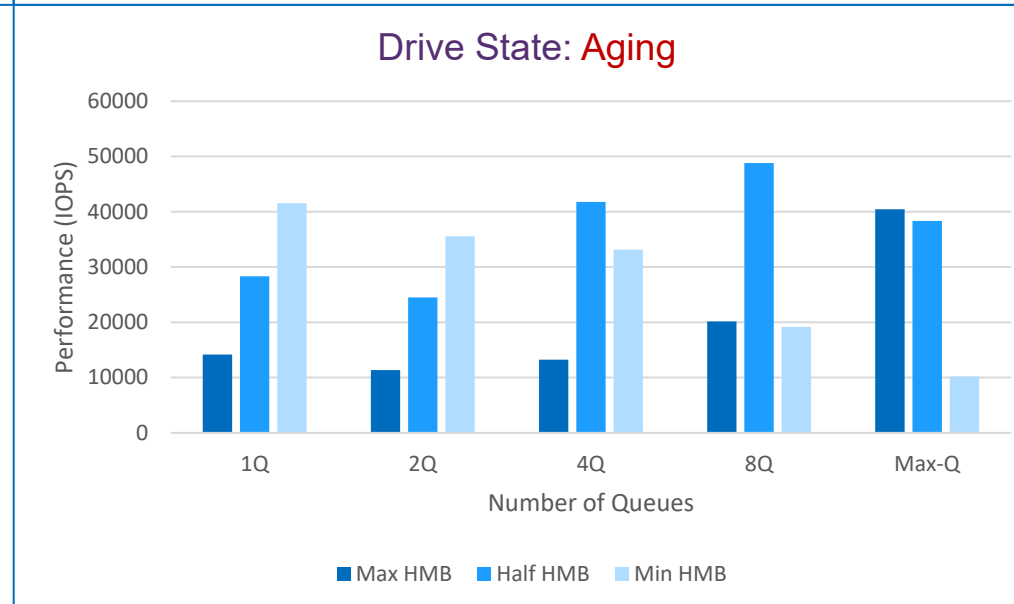


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Key Observations:

- Normal & Sustain state performance has no impact due to HMB Size variation
- Min HMB allocation shows Aging performance decreased with increase in Queues configured.
- Max HMB allocation shows Aging performance increased with increase in Queues configured.

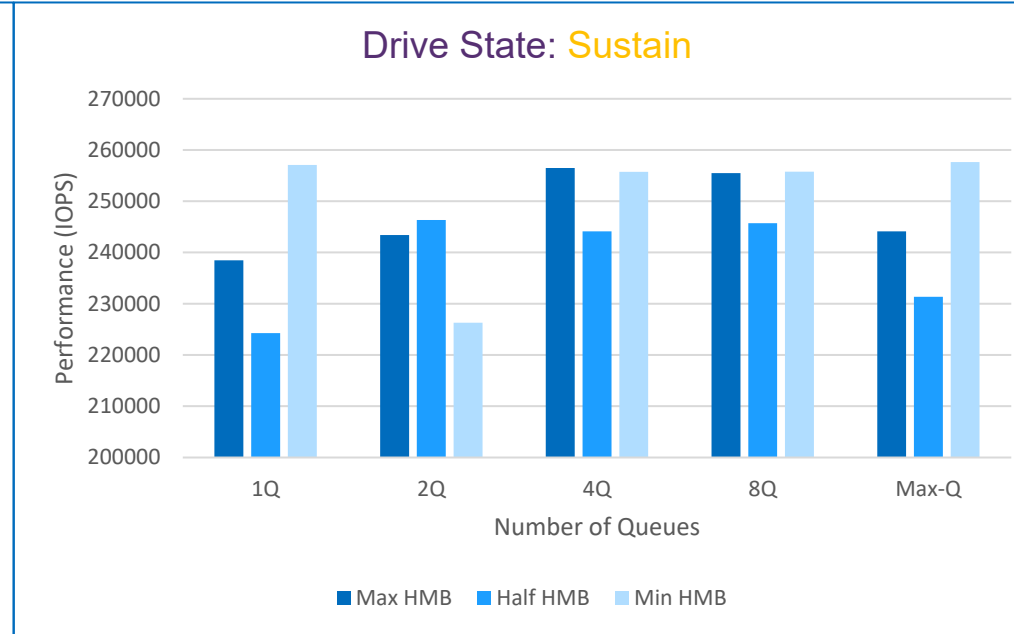
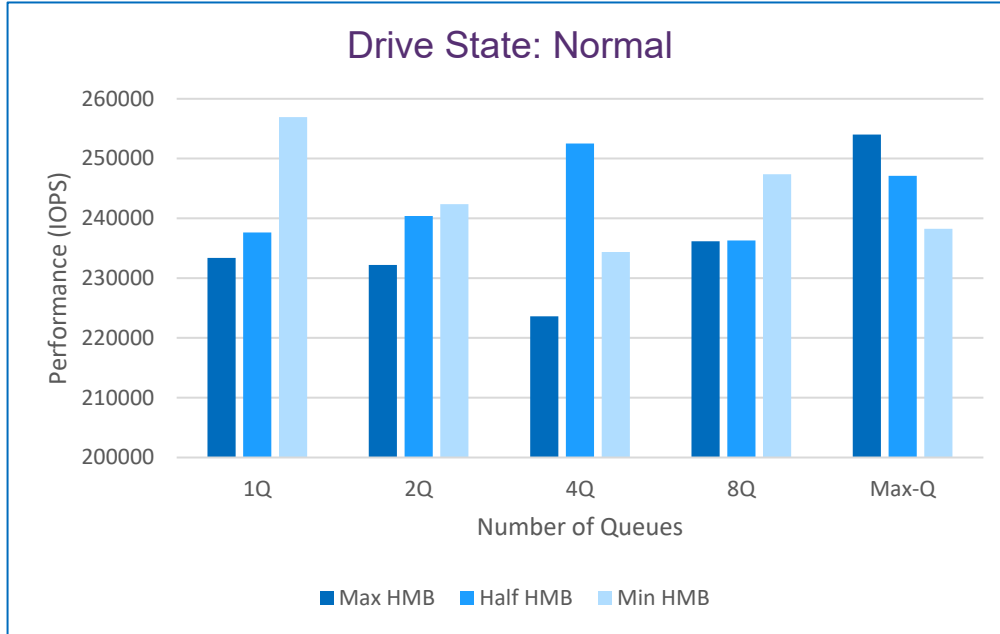


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Workloads: Random Write (4K-T16-QD32)

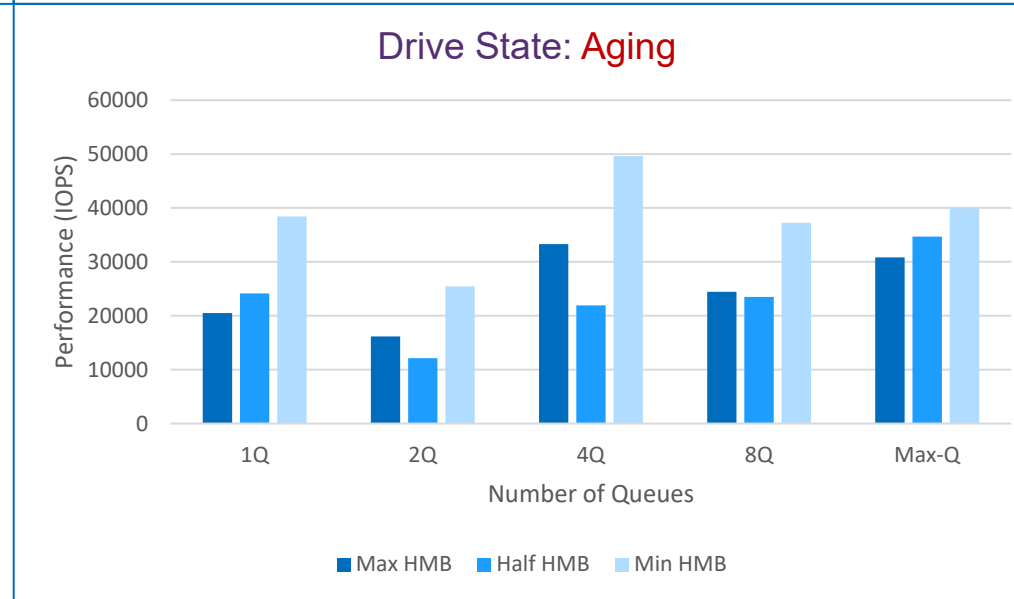


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Key Observations:

- Min HMB size allocation shows better results in majority of configurations & drive states
- Half HMB size allocation shows low performance in majority of cases in sustain & aging states

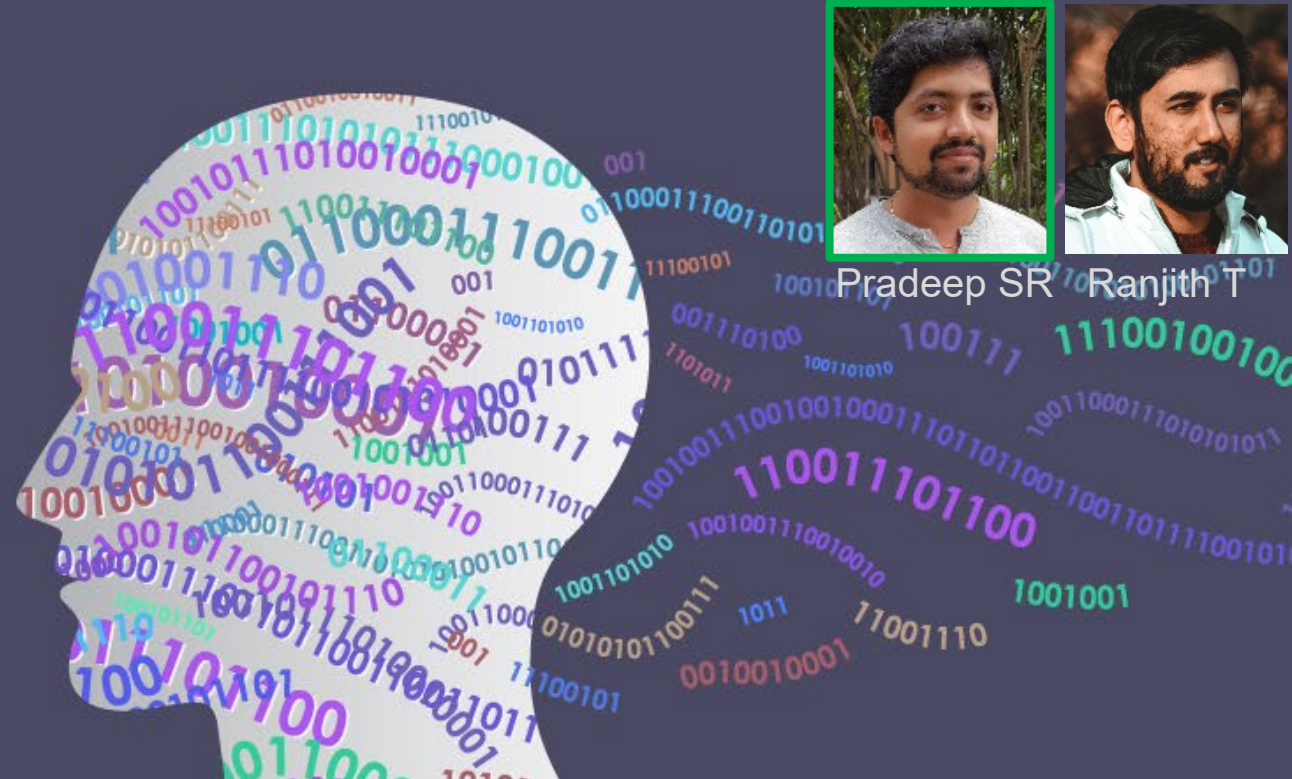


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Conclusion

Conclusion

- On Sequential Workloads DRAM leads much ahead of HMB
- On Random Read workloads HMB worked better to DRAM
- On Random Writes HMB is competitive enough, but lose sheen as drive ages
- HMB Worked best with Max-Q configuration bridging gap with DRAM
- HMB Size Variation didn't impact Sequential workloads much, Variations seen on Random workloads & Drive states
- Across all HMB Sizes allocated, 4Q configurations has been optimal.



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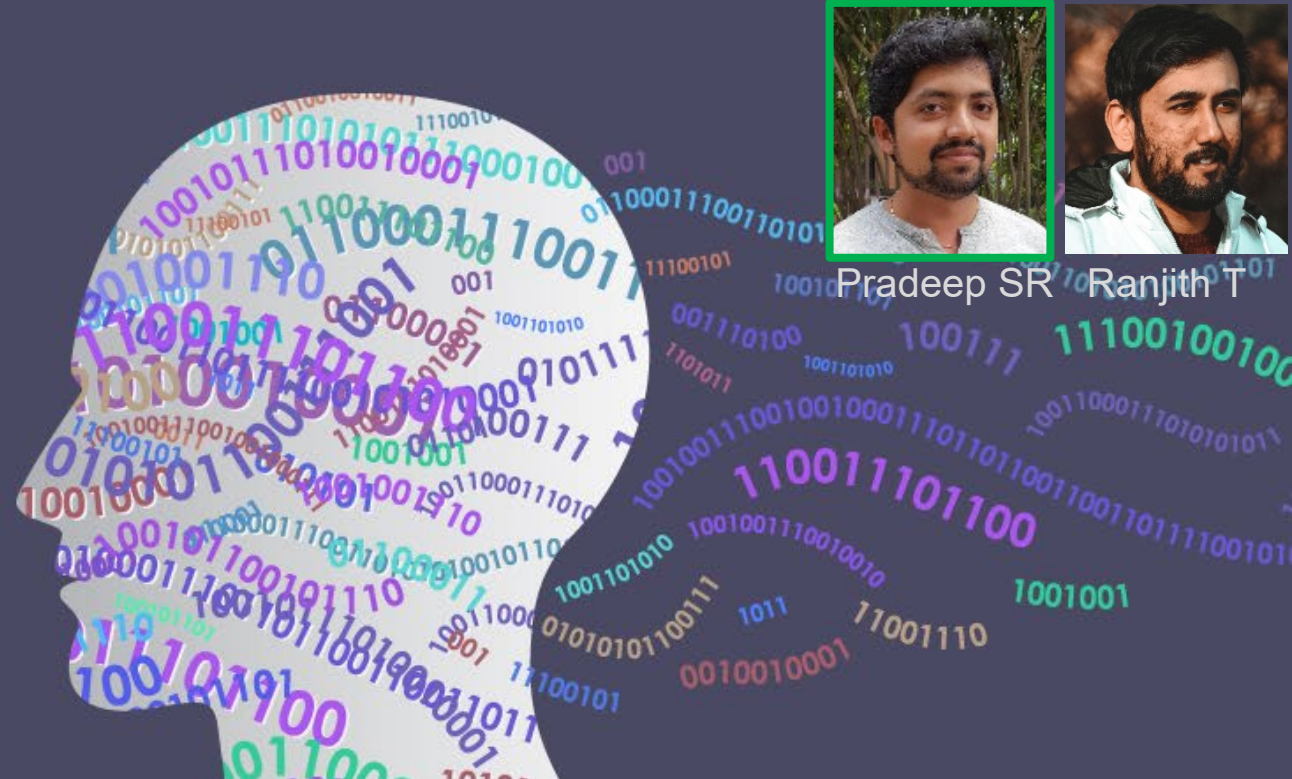


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