

## What's going on with NVMe? An examination of new technology adoption

Mike Scriber Sr. Director, Server Solution Management 9/23/2020





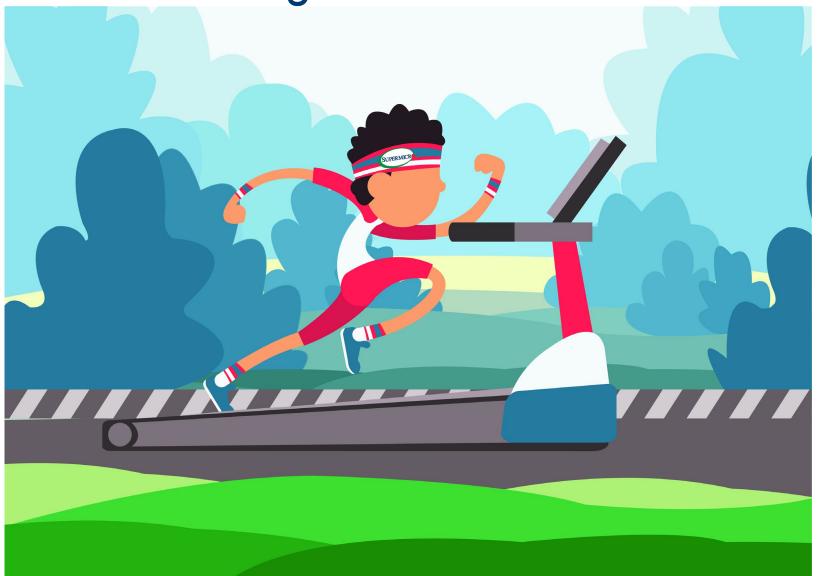
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## What's going on with NVMe?

- Our Industry Pace
- NVMe Growth
- More is Better
- Where is EDSFF going?
- What is QLC?
- Why NVMeoF?
- GPU Direct



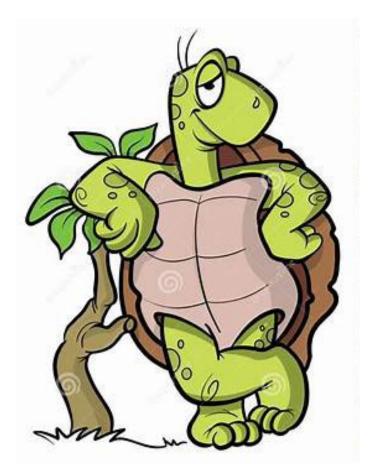
### We are driving fast and hard





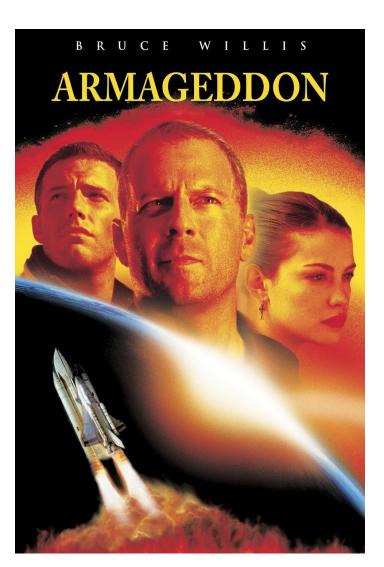
### Our customers are on their own pace

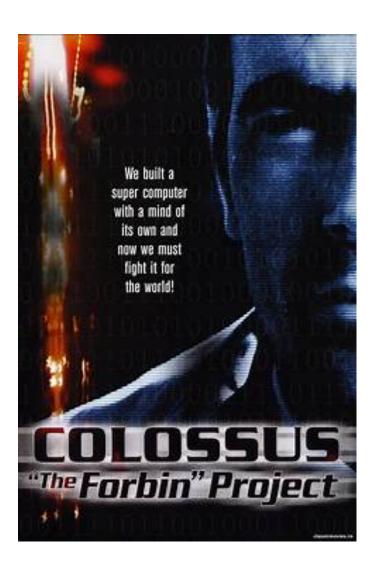


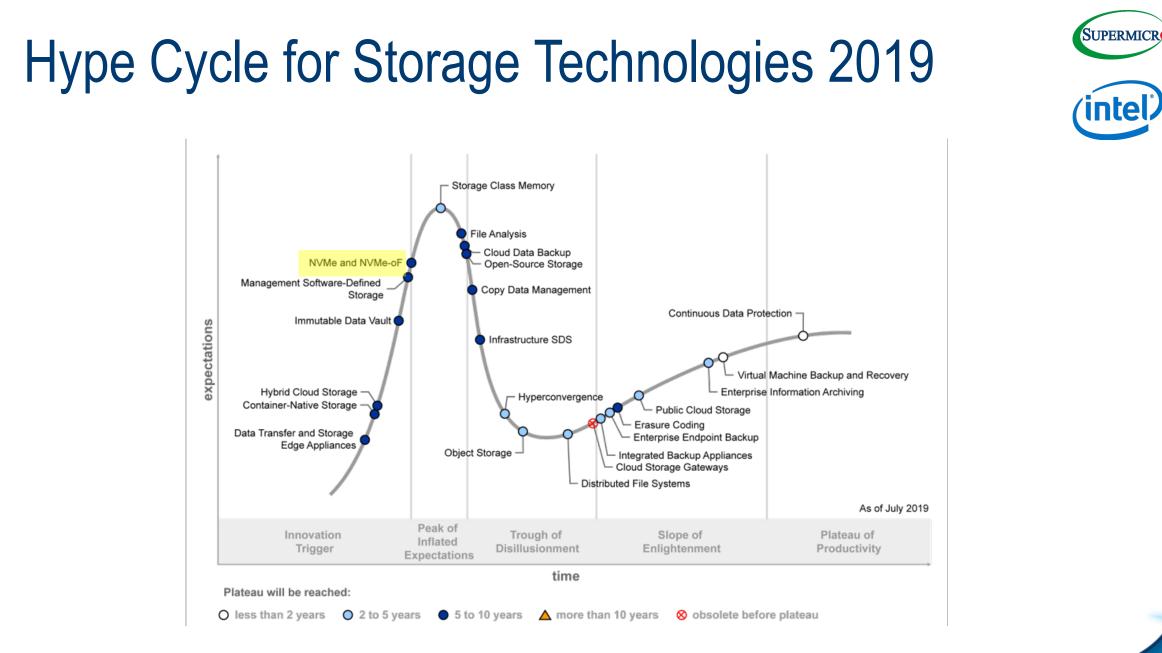


### Is technology just science fiction?









Source : https://www.gartner.com/doc/reprints?id=1-1YH750DY&ct=200225&st=sb

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## Will Flash Penetrate All Enterprise Storage?



- **IDC** reports by 2019, AFAs were generating almost **80%** of primary external storage revenues.
- Flash also brings benefits to the Secondary Storage
  - Performance
    - Higher throughput and bandwidth, the ability to move large data sets quickly
  - Capacity
    - Increased infrastructure density, reduce the floor space, energy and cooling capacity requirement and improve the overall TCO.
  - Reliability
    - No moving parts.

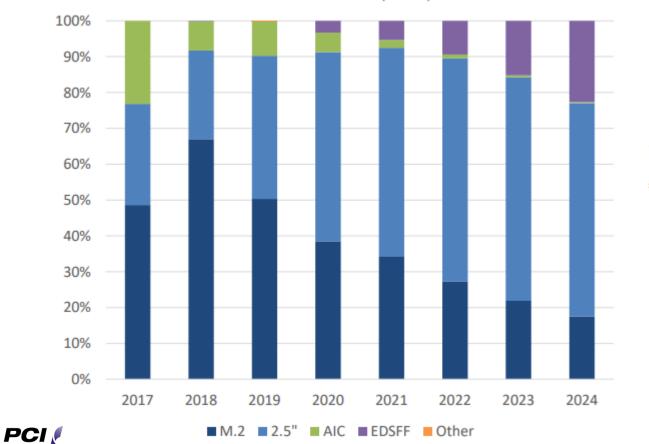


https://blogs.idc.com/2019/12/09/will-flash-penetrate-secondary-storage-environments/

### **Enterprise SSD Form Factor and Unit Trend**



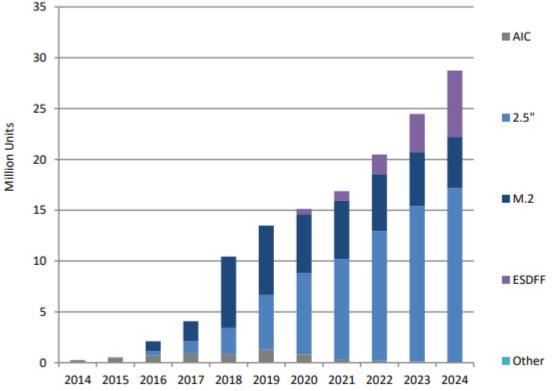




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### PCIe Form Factor (Units)

Data Center / Enterprise PCIe SSD Units



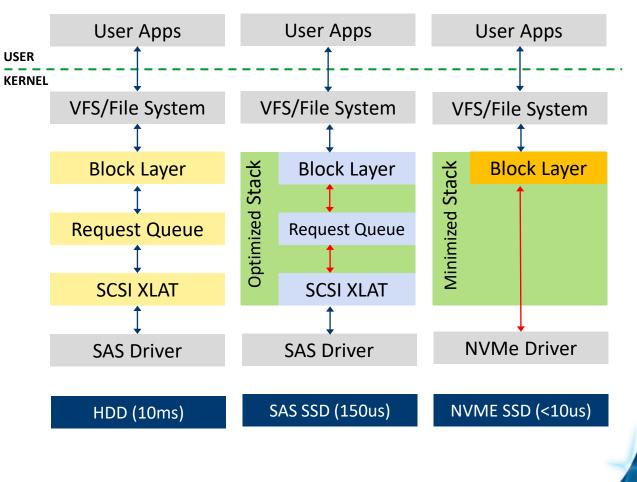


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### **NVMe Design Principle**

- Optimized protocol for NAND flash.
- NVMe bypasses unneeded layers.
- Direct connection to CPU's PCIe lanes.
- Dramatically reducing latency and increasing bandwidth.
- Scales with number of PCIe lanes
- No HBA required.

### **Evolution of Storage IO Stack**





Source : https://www.virtual.com/blog/i-o-i-o-its-nvme-i-go/

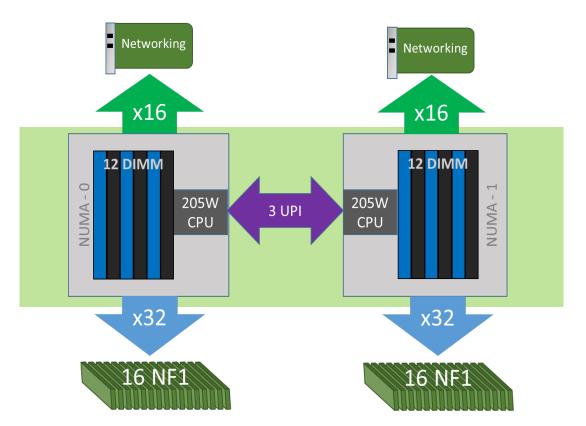
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### New CPUs are helping NVME

- More PCIe Lanes
- PCIe Gen 4 and above





### More is Better



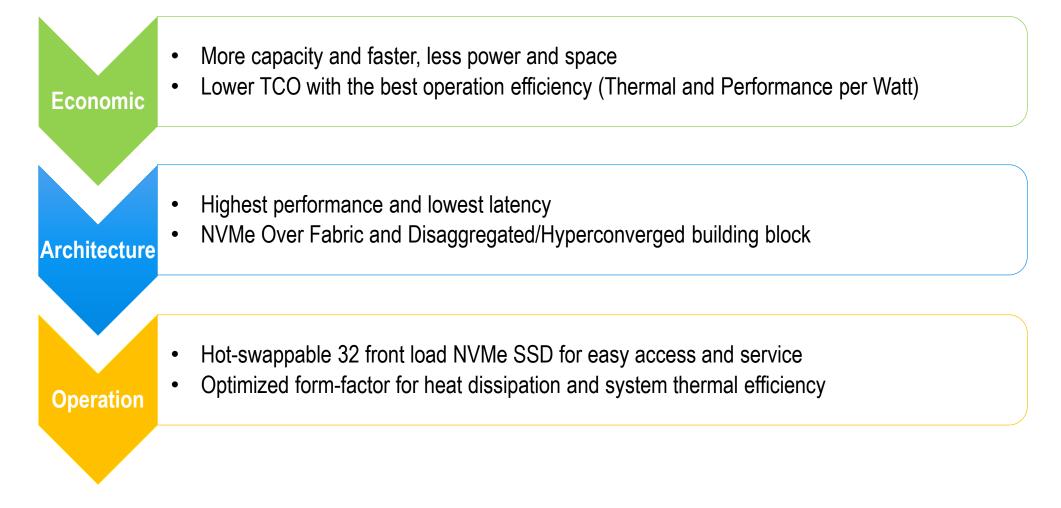
## X11 1U32 NVMe Optimized Storage Family Petascale NVMe Solution with Unprecedented Density and Performance



### **1U NVMe Petascale Advantages**

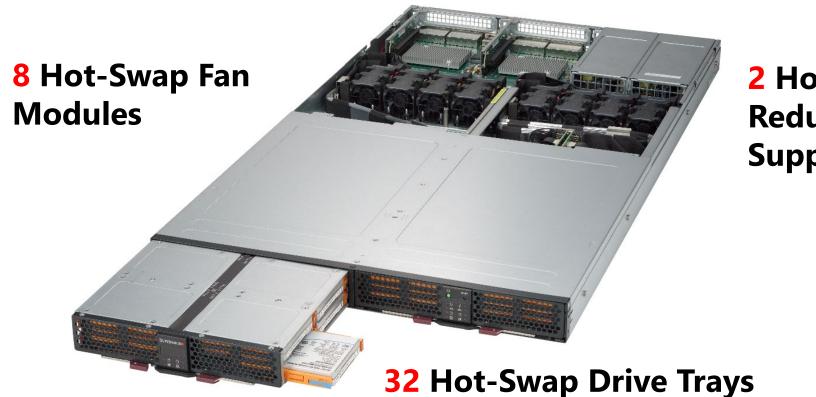






### Hot-Swap JBOF Design

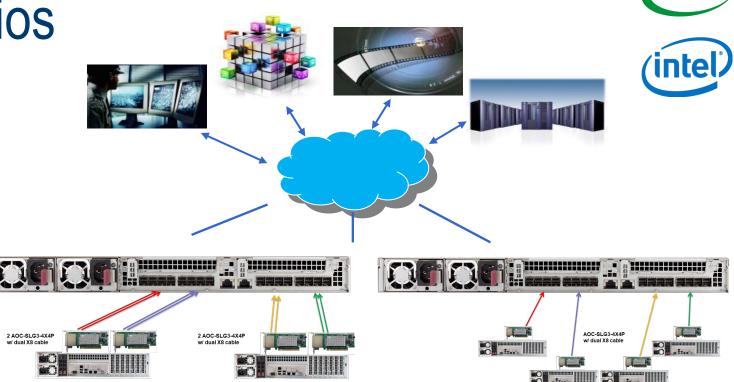


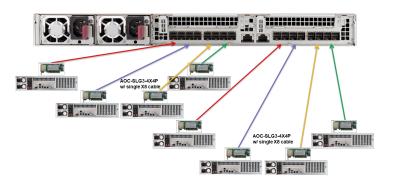


### 2 Hot-Swap Redundant Power Supplies

### **Application Scenarios**

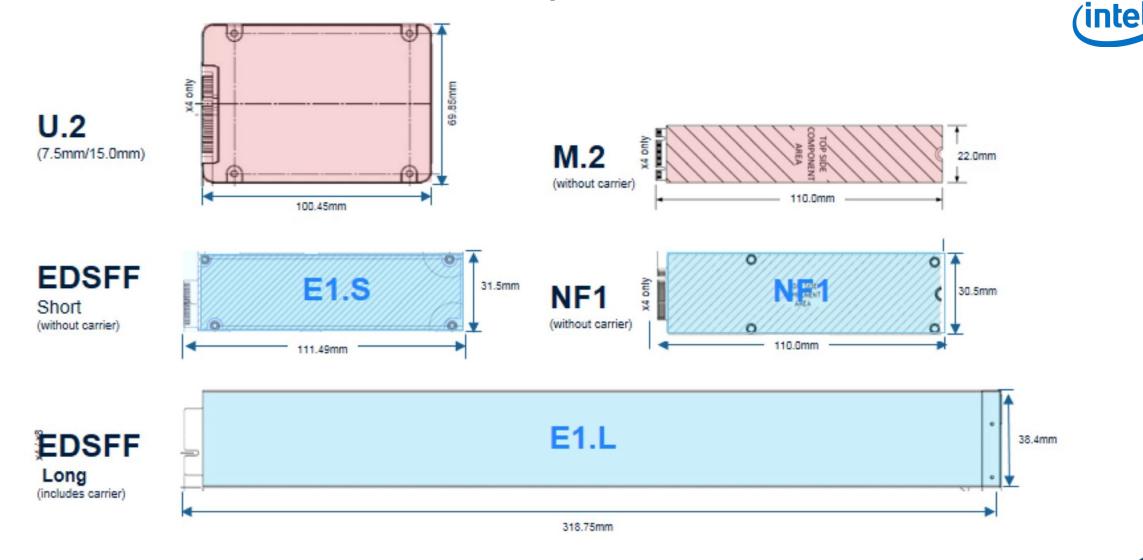
- High capacity storage requirements
  - High Throughput Ingest
  - High Density Hot Storage
  - HPC /Data Analytics
  - Media/Video Streaming
  - Content Delivery Network (CDN)
  - Big Data Top of Rack Storage





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### **NVMe Form Factor Comparison**



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### What is EDSFF\*?

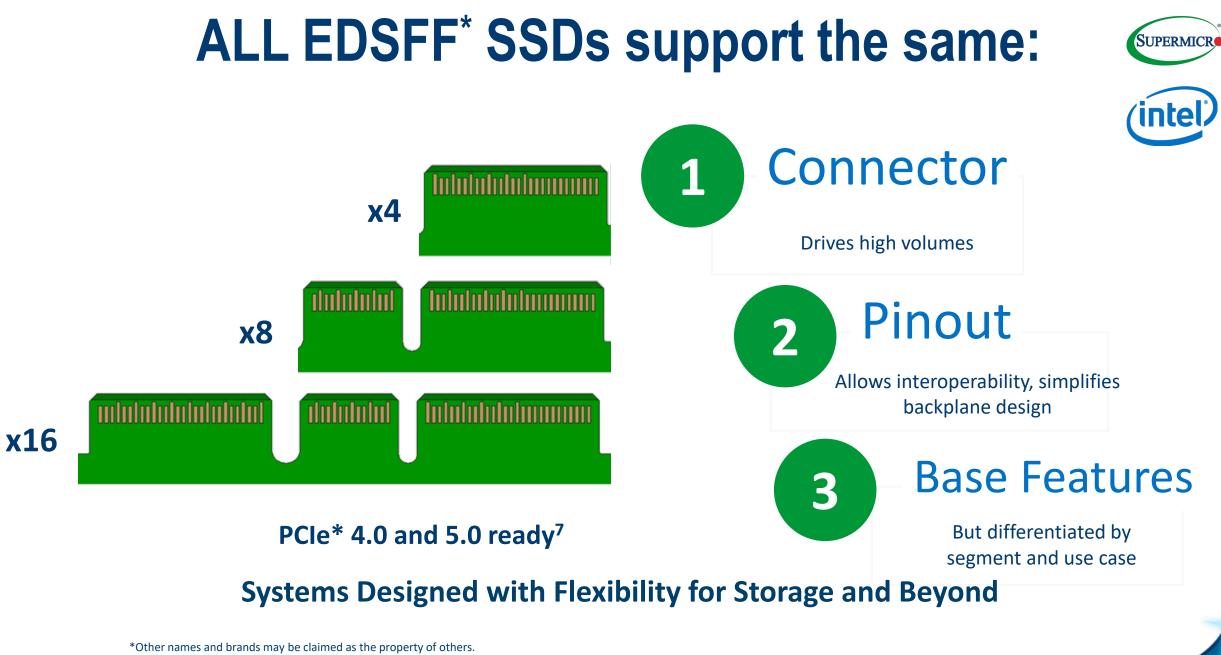


A group of **15** companies working together<sup>1</sup> 2 Industry standard connector and form factor **optimized** for NVMe\*

3 Built for increased operational efficiency and dense storage

### Intel<sup>®</sup> SSDs with EDSFF<sup>\*</sup> "ruler"





Source – Amphenol ICC\*. https://www.amphenol-icc.com/connect/cool-edge-high-speed-high-power-card-edge.html. Additional detail: https://EDSFFspec.org/introduction-to-EDSFF/

## EDSFF vs. 2.5" Storage Chassis Implementation

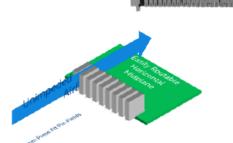
### 2.5" FORM FACTOR



Drive cages add cost, failure points

- Backplane requires cut outs to optimize thermals
- Cables add cost and complicate installation, thermals
- LED controller adds failure point

# RULER FORM FACTOR



- Eliminate the backplane
  Simplified thermal impleme
- Simplified thermal implementation
- No add in cards required
- No cables to SSDs
- Geographic drive mapping for simplified drive management

Less complicated chassis Reduced component cost per SSD Simple hot swap with high density capabilities

## High Efficiency by Design

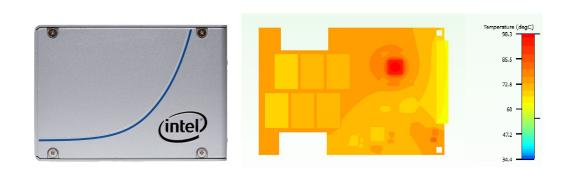


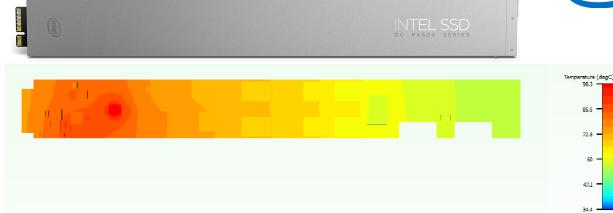
### • Better Air-flow = Better Power Efficiency

- Front Loading bays with increased Air-flow
- High Performance Up to 10 million IOPS in 1U
- Hot Plug and Power Loss Protection
- Capacity : 144 ~ 576TB

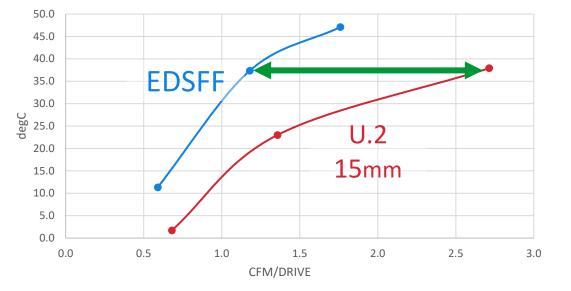
SSG-1029P-NEL32R

### Advantage. Thermal efficiency.













### EDSFF Long (E1.L) Form Factors





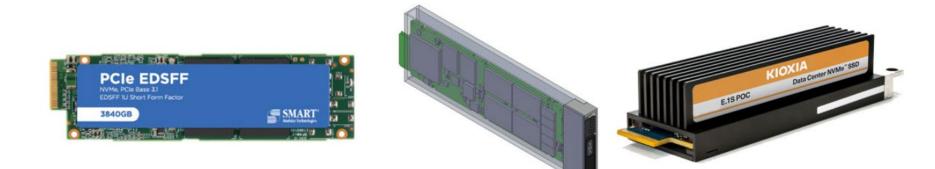
Illustrations left to right: E1.L 9.5mm (courtesy of Intel); E1.L 18mm (courtesy of Intel)

INITEL COD

Туре	Width	Length	Thickness
E1.L 9.5mm	up to 25W - 38.4mm	318.75mm	9.5mm
E1.L 18mm	up to 40W - 38.4mm	318.75mm	18mm

### EDSFF Short (E1.S) Form Factors



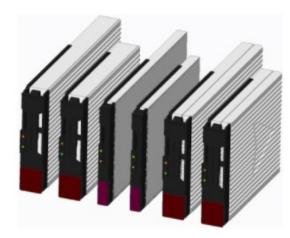


Illustrations left to right: E1.S 5.9mm (courtesy of SMART Modular Systems); E1.S Symmetric Enclosure (courtesy of Intel); E1.S Asymmetric Enclosure (courtesy of KIOXIA)

Туре	Width	Length	Thickness
E1.S 5.9mm	31.5mm	111.49mm	5.9mm
E1.S 8mm heat spreader	31.5mm	111.49mm	8.01mm
E1.S Symmetric Enclosure	33.75mm	118.75mm	9.5mm
E1.S Asymmetric Enclosure	33.75mm	118.75mm	15mm
E1.S Asymmetric Enclosure	33.75mm	118.75mm	25mm

### EDSFF 3 (E3) Form Factors





Illustrations left to right: various E.3 configurations (courtesy of Intel)

Туре	Width	Length	Thickness
E3.S 7.5mm	76mm	104.9mm	7.5mm thickness
E3.S 16.8mm	76mm	104.9mm	16.8mm
E3.L 7.5mm	76mm	142.2mm	7.5mm
E3.L 18mm	76mm	142.2mm	16.8mm

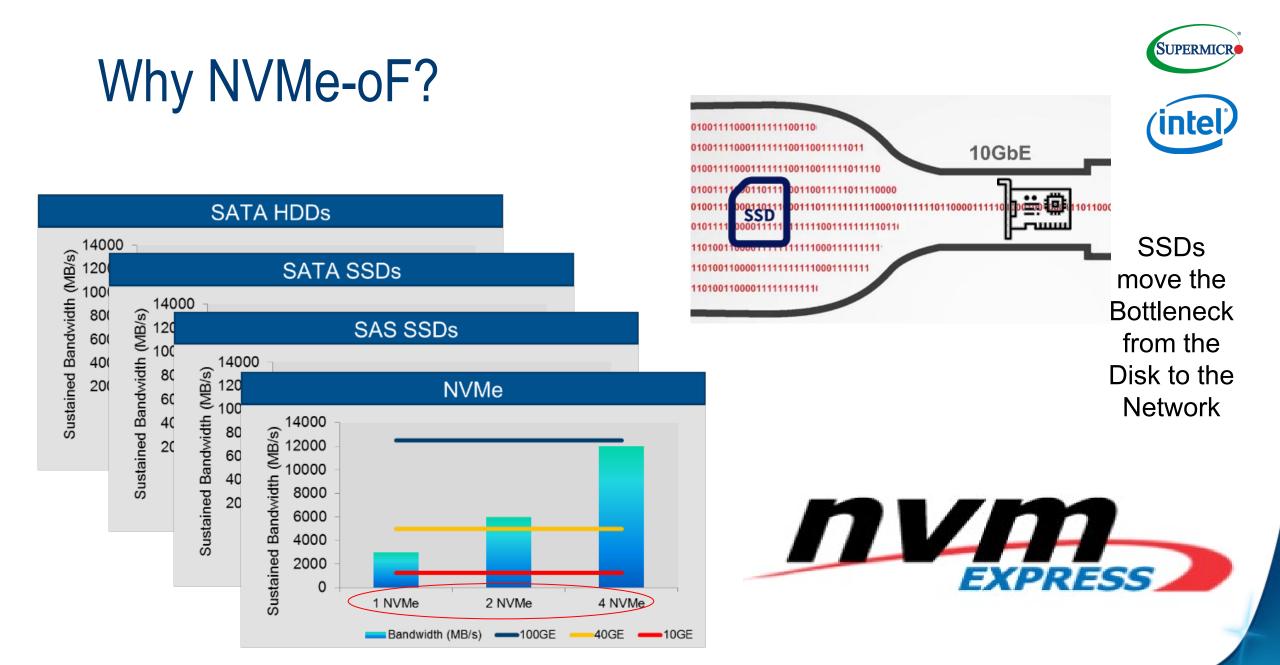
## QLC vs TLC

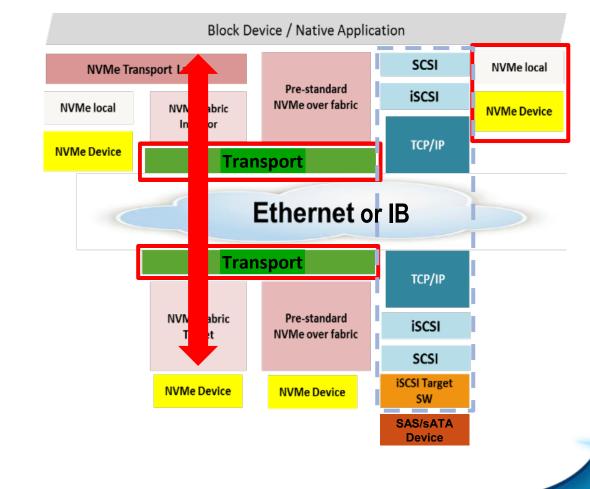
- QLC has 4-bits per cell, while TLC has 3-bits per cell.
  - 33% capacity improvement
- QLC costs less than TLC
  - Closing the price gap between SSDs and HDDs
- QLC EDSFF using 16K block writes
- QLC has slower write performance, but same read performance.
- QLC EDSFF endurance is <0.5 DWPD
  - 8TB drive \* 1 DWPD = 8TB per day
  - 16TB drive \* .5 DWPD = 8TB per day

QLC is best for read intensive applications







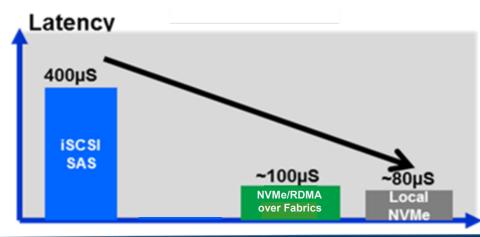


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By extending NVMe efficiency over a fabric
 NVMe commands and data structures are

- NVMe commands and data structures are transferred end to end
- Bypassing legacy stacks for performance
- First products all used RDMA
- Performance is impressive

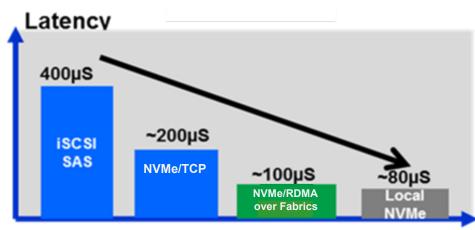


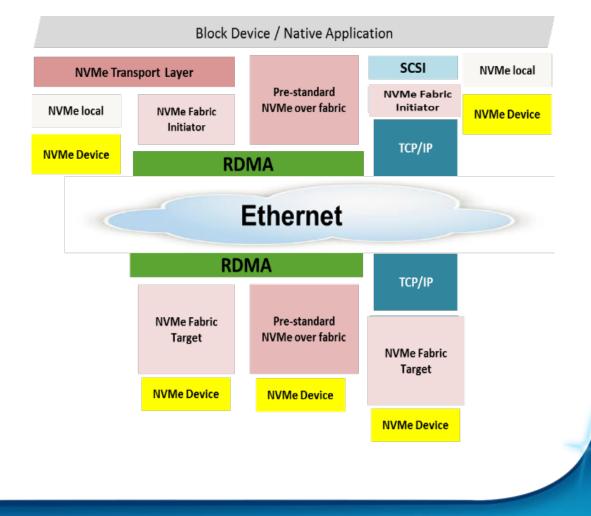
### How Does NVMe-oF Maintain NVMe Like Performance?



fabricNVMe commands and data structures are

- transferred end to end
- Bypassing legacy stacks for performance
- First products all used RDMA
- Performance is impressive





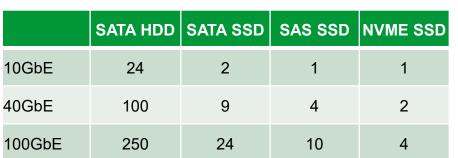
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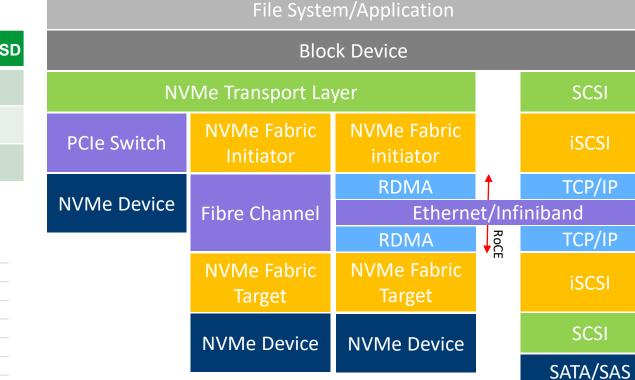
### What and Why is NVMe over Fabrics



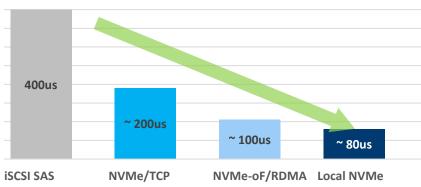
(intel)



Number of SSDs to Saturated Network BW



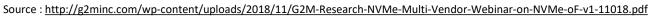
Latency (us)



ustrial automation (article (21805-121 /nume over fabric addresses hyperscale storage needs (

Source : https://www.electronicdesign.com/industrial-automation/article/21805431/nvme-over-fabric-addresses-hyperscale-storage-needs/

Device

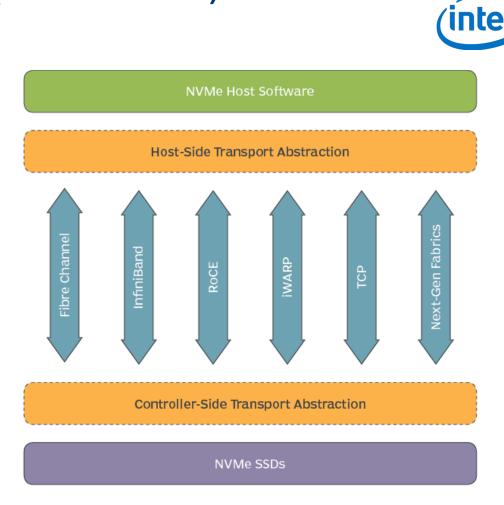


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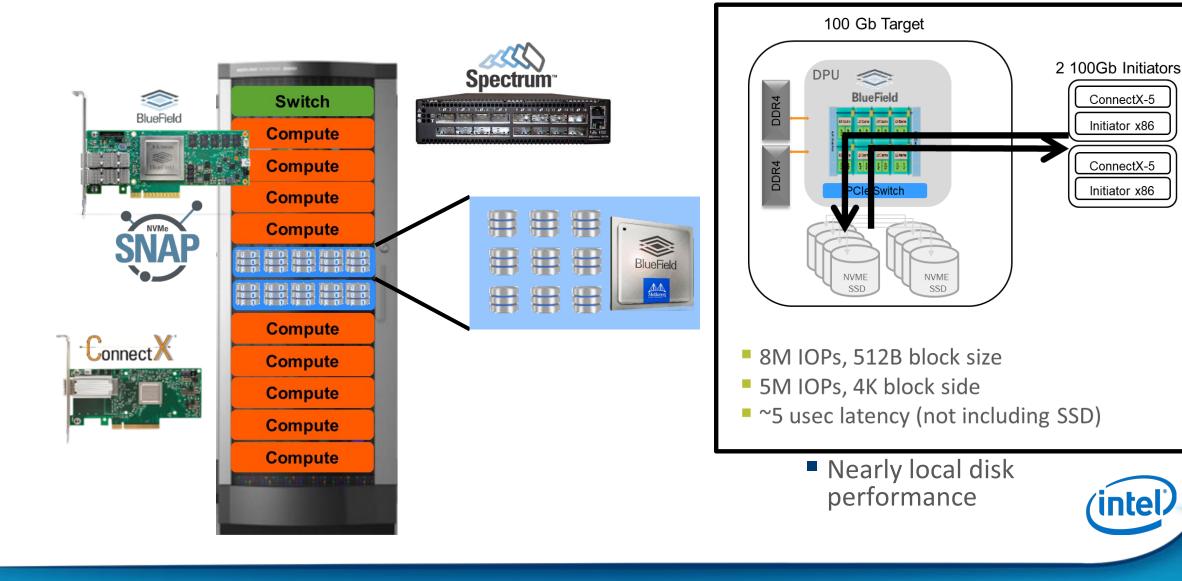
### What is NVMe over Fabrics (NVMe-oF)

- A protocol interface to NVMe that enable operation over other interconnects (e.g., Ethernet, InfiniBand, Fibre Channel).
- Shares the same base architecture and NVMe Host Software as PCIe.
- Enables NVMe Scale-Out and low latency (<10µS latency) operations on Data Center Fabrics.
- Avoids protocol translation overhead (avoid SCSI)

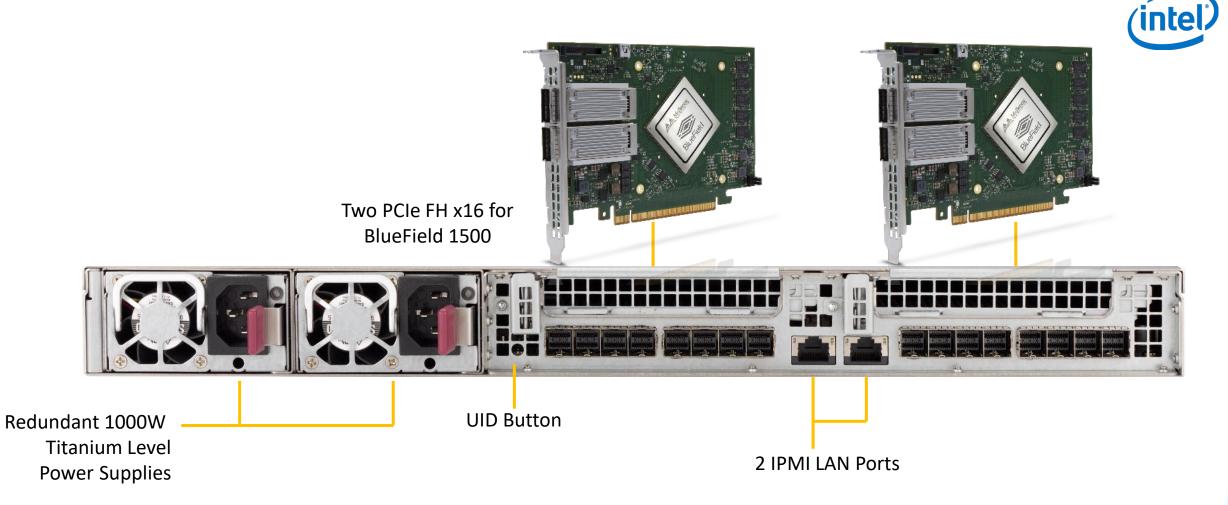




### NVMe-oF Applications - Composable Infrastructure



### NVMeoF JBOF (Rear View)



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### SSDs are going to become much faster

- 3D Xpoint Memory, 3D NAND, etc.
- PMEM, Storage Class Memory, etc.
- from data

Faster storage access delivers more value

The value of data is based on how fast it can

of data Faster storage access enables cost reduction through consolidation

The cost of data-at-rest is no longer the

right metric for storage TCO

be accessed and processed

- NVMe over Fabrics increases the velocity

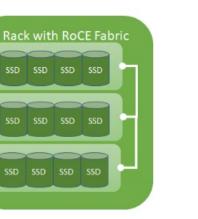


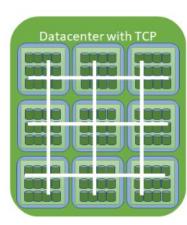
NVMe over internal

PCIe: 10's of drives

The Value of Shared Storage and The 'Need for Speed'







NVMe/TCP: Low Latency

Highly scalable ethernet

fabric 10,000's of drives



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### **GPUDirect Storage**

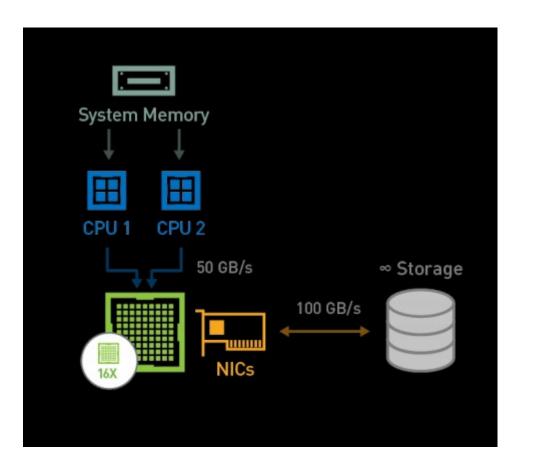
- Avoid copying through a CPU bounce buffer
- Performance
  - Raw IO bw difference varies by platform , e.g. 2-4X
  - Savings in memory management and utilization can be a force multiplier on top of the
  - Varies by platform
- Broad ecosystem interest, active enabling
- Enabling with broader Linux community
- Coming to a CUDA near you

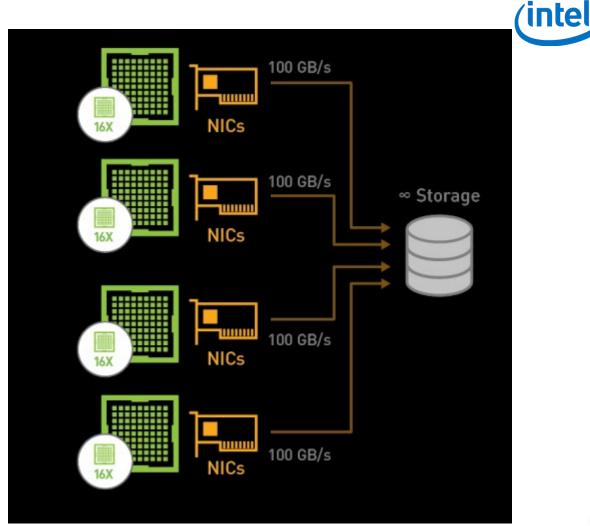
	$\Box \rightarrow \blacksquare$ $\Box \rightarrow \Box$ $\Box \rightarrow \Box$ $\Box \rightarrow \Box$ $\Box \rightarrow \Box$
GPU	GPU
Without GPUDirect Storage	With GPUDirect Storage
System Memory	NVMe PCIe Switch
GPUDirect Storage	Bounce buffer PCIe

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### **GPUDirect Storage and Cluster**

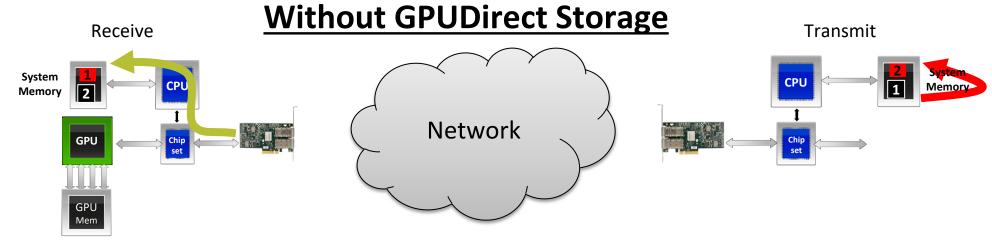


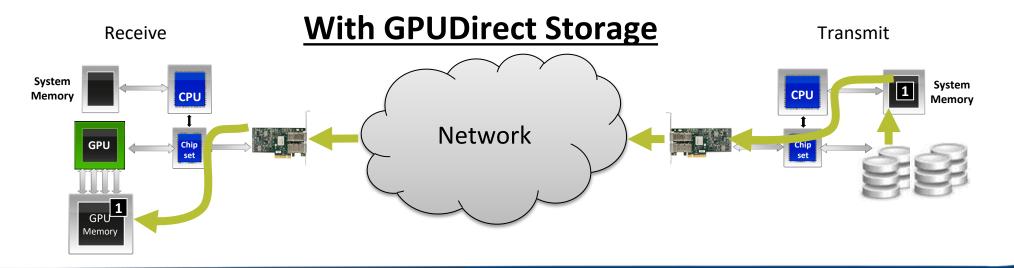




### **GPUDirect with RDMA**







### NVMe is on the Move with Innovation

- NVMe is growing and changing
- Processors are enabling better NVMe systems
- EDSFF will take over, if we can settle down the spec
- NVMeoF enables low latency transfer of data directly into the drives.
- GPU direct allows access to NVMe drives without the CPU.
- Customers need to know that this is not science fiction.
- Supermicro has products for everything that I have discussed.





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